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TITLE :EV270QUM-N10

Product Specification

Rev. P1

HEFEI XINSHENG OPTOELECTRONICS TECHNOLOGY

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		REVISION HISTORY		
REV.	ECN No.	DESCRIPTION OF CHANGES	DATE	PREPARED
Rev. P0		Preliminary specification	2018.10.17	LiMingWei
		BL Power Consumption	2019.2.15	Liu Danjuan
Rev. P1	Converter Electrical Characteristics		2019.2.15	Liu Danjuan
Rev. P1	Weight 3230->3340		2019.2.15	Tian Zhaoshou
		LED structure	2019.2.15	Liang Fuxue
		~~		

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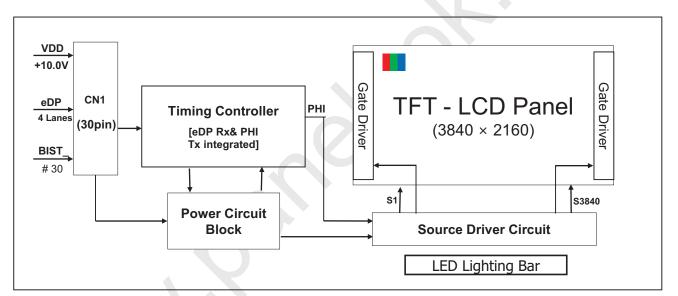
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2.0	Absolute Maximum Ratings			
3.0	Electrical Specifications	7~8		
4.0	Optical Specifications	9~10		
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1.0 GENERAL DESCRIPTION

1.1 Introduction

EV270QUM-N10 is a color active matrix TFT LCD module using amorphous silicon TFT's (Thin Film Transistors) as an active switching devices. This module has a 27 inch diagonally measured active area with UHD resolutions (3840 horizontal by 2160 vertical pixel array). Each pixel is divided into RED, GREEN, BLUE dots which are arranged in vertical stripe and this module can display 1.07B colors. The TFT-LCD panel used for this module is adapted for a low reflection and higher color type.



1.2 Features

- Reverse Type
- 4 lane eDP Interface with 5.4Gbps Link Rates
- 10bit (8bit+A-FRC) color depth, display 1.07B colors
- Incorporated edge type back-light (LED)
- Compatible with NTSC90% and sRGB Matching Ratio 98%(Typ.) & Area Ratio 105%(Typ.)
- High luminance and contrast ratio, low reflection and wide viewing angle
- RoHS/Halogen Free
- Gamma Correction



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1.3 Application

- Desktop Type of PC & Workstation Use
- Slim-Size Display for Stand-alone Monitor
- Display Terminals for Control System
- Monitors for Process Controller

1.4 General Specification

The followings are general specifications at the model EV270QUM-N10.

<Table 1. General Specifications>

Parameter	Specification	Unit	Remarks
Active area	596.736(H) × 335.664 (V)	mm	
Number of pixels	3840(H) ×2160 (V)	pixels	
Pixel pitch	$0.1554(H) \times 0.1554(V)$	mm	
Pixel arrangement	RGB Vertical stripe		
Color Depth	1.07 B(8bit+A-FRC)	colors	
Display mode	Normally Black		
Dimensional outline	630.0(H) ×368.2 (V) × 20.98(Depth)	mm	Detail refer to drawing
Weight	3340 (typical)	g	
Bezel width (L/R/U/D)	13.71/13.71/13.74/13.74	mm	
Surface Treatment	Haze 25%, 3H		
Back-light	Long side, 2-LED Lighting Bar type		

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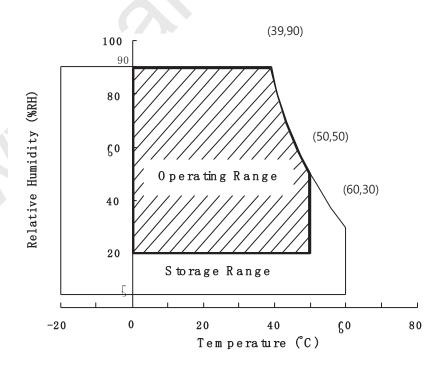
2.0 ABSOLUTE MAXIMUM RATINGS

The followings are maximum values which, if exceed, may cause faulty operation or damage to the unit. The operational and non-operational maximum voltage and current values are listed in Table 2.

< Table 2. Absolute Maximum Ratings> [VSS=GND=0V]

Parameter	Symbol	Min.	Max.	Unit	Remarks
Power Supply Voltage	V_{DD}	GND-0.3	12	V	
Logic Supply Voltage	V _{IN}	VSS-0.3	V _{DD} +0.3	V	Ta = 25 °C
Operating Temperature	T _{OP}	0	+50	$^{\circ}\mathbb{C}$	1)
Storage Temperature	T _{ST}	-20	+60	C	1)

Note: 1) Temperature and relative humidity range are shown in the figure below. Wet bulb temperature should be 39 °C max. and no condensation of water.





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3.0 ELECTRICAL SPECIFICATIONS

3.1 Electrical Specifications

< Table 3. Electrical specifications >

[Ta = 25 ± 2 °C]

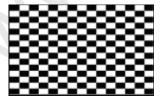
Parameter		Min.	Тур.	Max.	Unit	Remarks
Power Supply Voltage	V_{DD}	9	10.0	11	V	N-4-1
Power Supply Current	I_{DD}	-	460	900	mA	Note1
In-Rush Current	I_{RUSH}	-	2.0	3.0	A	Note 2
Permissible Input Ripple Voltage	V _{RF}	-	-	400	mV	Note1,3
High Level Differential Input Threshold Voltage	V_{IH}	-	-	+100	mV	
Low Level Differential Input Threshold Voltage	V _{IL}	-100	·)-	mV	
Differential input voltage	V _{ID}	100	-	600	mV	
Differential input common mode voltage	Vem	0		2		V_{IH} =100mV, V_{IL} =-100mV
	P _D		4.6	9	W	
Power Consumption	P_{BL}	-	55	58	W	Note 4
	P _{total}	-	59.6	-	W	

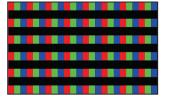
Notes: 1. The supply voltage is measured and specified at the interface connector of LCM.

The current draw and power consumption specified is for VDD=10.0V, Frame rate=60Hz

Test Pattern of power supply curren

a) Typ : Mosaic Patternb) Max : 1 line Inversion

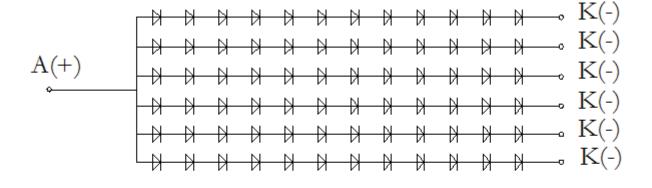


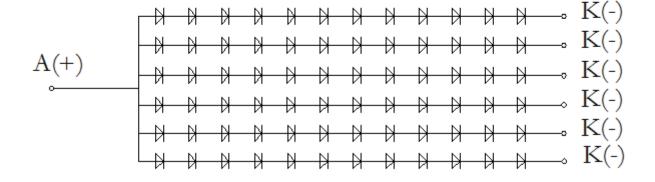


- 2. Duration of rush current is about 2 ms and rising time of VDD is 520 μ s \pm 20 %
- 3. Ripple Voltage should be covered by Input voltage Spec.
- 4. Calculated value for reference (Input pins*VPIN ×IPIN) excluding converter loss.

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3.2 LED structure





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3.3 Backlight Unit

< Table 4. LED Backlight Unit >

Parameter	Min.	Тур.	Max.	Unit	Remarks	
LED Light Bar Input Voltage Per Input Pin	VPIN	33.6	37.2	39.6	V	Duty 100%
LED Light Bar Input Current Per Input Pin	IPIN	-	110		mA	Note1,2,
LED Power Consumption	P_{BL}	-	55	58	W	Note 3
LED Life-Time	-	30,000		_	Hrs	Note 4

LED bar consists of 144 LED packages,12 strings(parallel)12packages(serial)

Note1: There are one light bar ,and the specified current is input LED chip 100% duty current

Note2: The sense current of each input pin is 110mA

Note3: $P_{BL}=12Input pins*VPIN \times IPIN$

Note4: The lifetime is determined as the time at which luminance of LED become 50% of the initial brightness or not normal lighting at IPIN=110mA on condition of continuous operating at 25 ± 2 °C



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3.4 Converter Electrical Characteristics

< Table 5. Converter Electrical Characteristics >

Parameter		Min.	Тур.	Max.	Unit	Remarks
Converter Power Supply Voltage	V_{i}	21.6	24.0	26.4	V	(Duty 100%)
Converter Power Supply Current	I_{i}	-	2.2	-	A	(Duty 100%)
In-Rush Current	I _{RUSH LED}	-	-	6.0	A	(Duty 100%)
BL Control Level	EN(ON)	2.0	3.3	5.0	V	
BL Control Level	EN(OFF)	0	0	0.8	V	
PWM Control Level	PWM (High Level)	2.0	3.3	5.0	V	Positive Dimming
PWM Control Level	PWM (Low Level)	0	0	0.8	V	
PWM Control Duty Ratio		10	-	100	%	
PWM Control Frequency		100	200	500	Hz	

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4.0 OPTICAL SPECIFICATION

4.1 Overview

The test of Optical specifications shall be measured in a dark room (ambient luminance ≤ 1 lux and temperature = $25\pm 2^{\circ}C$) with the equipment of Luminance meter system (Goniometer system and TOPCONE PR730) and test unit shall be located at an approximate distance 50cm from the LCD surface at a viewing angle of θ and Φ equal to θ °. We refer to $\theta_{\emptyset=0}$ (= θ_3) as the 3 o'clock direction (the "right"), $\theta_{\emptyset=90}$ (= θ_{12}) as the 12 o'clock direction ("upward"), $\theta_{\emptyset=180}$ (= θ_9) as the 9 o'clock direction ("left") and $\theta_{\emptyset=270}$ (= θ_6) as the 6 o'clock direction ("bottom"). While scanning θ and/or \emptyset , the center of the measuring spot on the Display surface shall stay fixed. The measurement shall be executed after 30 minutes warm-up period. VDD shall be 5.0V +/-10% at 25°C. Optimum viewing angle direction is 6 'clock.

4.2 Optical Specifications

[VDD = 10.0V, Frame rate = 60Hz, Clock = 74.25MHz, I_{BL} = 1320mA, Ta =25±2 $^{\circ}$ C] < Table 6. Module Optical >

			_				_	
Paramete	er	Symbol	Condition	Min.	Тур.	Max.	Unit	Remark
	TT ' . 1	Θ_3		85	89	-	Deg.	
57' ' A 1	Horizontal	Θ_9	CR > 10	85	89	-	Deg.) N
Viewing Angle range	Vertical	Θ_{12}	CR > 10	85	89	-	Deg.	Note I
	vertical	Θ_6		85	89	-	Deg.	Note 1 Note 2 Note 3 Note 4
Luminance Contrast ra	tio	CR		700	1000			Note 2
Luminance of White		Y _w		800	1000	-	cd/m ²	Note 3
White luminance unifo	ormity	ΔΥ		80	85		%	Note 4
	White	W _x	$\Theta = 0^{\circ}$ (Center)	0.283	0.313	0.343	-	
	white	W _y		0.299	0.329	0.359	-	
	Red	R _x	Normal	0.646	0.676	0.706	-	
Reproduction	Red	R _y	Viewing Angle	0.269	0.299	0.329	-	Note 5
of color	Green	G_x		0.197	0.227	0.257	-	Note 5
	Green	G_{y}		0.649	0.679	0.709	-	
	Dlas	B _x		0.123	0.153	0.183	-	
	Blue	B _y		0.029	0.059	0.089	-	
Response Time	GTG	$T_{\rm g}$			14	20	ms	Note 6
Cross Ta	lk	CT		-	-	2.0	%	Note 7

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Note:

- 1. Viewing angle is the angle at which the contrast ratio is greater than 10. The viewing are determined for the horizontal or 3, 9 o'clock direction and the vertical or 6, 12 o'clock direction with respect to the optical axis which is normal to the LCD surface.
- 2. Contrast measurements shall be made at viewing angle of θ = 0° and at the center of the LCD surface. Luminance shall be measured with all pixels in the view field set first to white, then to the dark (black) state. (See FIGURE 1 shown in Appendix) Luminance Contrast Ratio (CR) is defined mathematically.

- 3. Center Luminance of white is defined as the LCD surface. Luminance shall be measured with all pixels in the view field set first to white. This measurement shall be taken at the locations shown in FIGURE 2 for a total of the measurements per display.
- 4. The White luminance uniformity on LCD surface is then expressed as : $\Delta Y = ($ Minimum Luminance of 9points / Maximum Luminance of 9points) * 100 (See FIGURE 2 shown in Appendix).
- 5. The color chromaticity coordinates specified in Table 5. shall be calculated from the spectral data measured with all pixels first in red, green, blue and white. Measurements shall be made at the center of the panel.
- 6. Response time Tg is the average time required for display transition by switching the input signal as below table and is based on Frame rate fV =60Hz to optimize.

 Each time in below table is defined as appendix Figure 3 and shall be measured by switching the input signal for "any level of gray(bright)" and "any level of gray(dark)"
- 7. Cross-Talk of one area of the LCD surface by another shall be measured by comparing the luminance (Y_A) of a 25mm diameter area, with all display pixels set to a gray level, to the luminance (Y_B) of that same area when any adjacent area is driven dark. (See FIGURE 4 shown in Appendix).



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5.0 INTERFACE CONNECTION.

5.1 Electrical Interface Connection

• CN1 Module Side Connector: STM MSAK24025P30 or Equivalent

Pin No	Symbol	Function	Remark
1	VDD	Power Supply (10.0V)	
2	VDD	Power Supply (10.0V)	
3	VDD	Power Supply (10.0V)	
4	VDD	Power Supply (10.0V)	
5	VDD	Power Supply (10.0V)	
6	GND	Ground	
7	GND	Ground	
8	NC	SCL PGMA	
9	NC	SDA PGMA	
10	GND	Ground	
11	HPD	Hot Plug Detection Signal	
12	GND	Ground	
13	DAUXN	Negative Signal for Auxiliary Chanel	
14	DAUXP	Positive Signal for Auxiliary Chanel	
15	GND	Ground	
16	DRX0P	Positive Signal For eDP Lane0	
17	DRX0N	Negative Signal For eDP Lane0	
18	GND	Ground	
19	DRX1P	Positive Signal For eDP Lane1	
20	DRX1N	Negative Signal For eDP Lane1	
21	GND	Ground	
22	DRX2P	Positive Signal For eDP Lane2	
23	DRX2N	Negative Signal For eDP Lane2	
24	GND	Ground	
25	DRX3P	Positive Signal For eDP Lane3	
26	DRX3N	Negative Signal For eDP Lane3	
27	GND	Ground	
28	GND	Ground	
29	NC	No connection	
30	BIST	BIST Function	BIST

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5.0 INTERFACE CONNECTION.

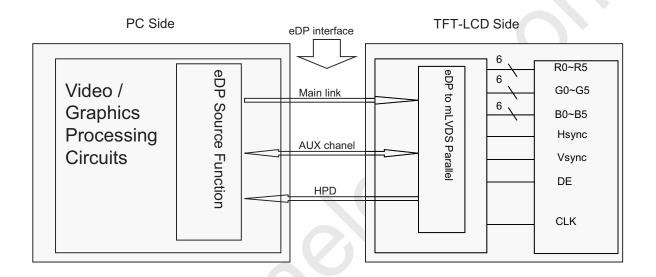
5.2 Electrical Interface Connection

• CN101 BLU Side Connector: CviLux CIO114M1HR0-NH or Equivalent

Pin No	Symbol	Function	Remark
1	VBL	Power Supply (24.0V)	
2	VBL	Power Supply (24.0V)	
3	VBL	Power Supply (24.0V)	
4	VBL	Power Supply (24.0V)	
5	VBL	Power Supply (24.0V)	
6	GND	Ground	
7	GND	Ground	
8	GND	Ground	
9	GND	Ground	
10	GND	Ground	
11	NC	No connection	
12	EN	Enable(0V:Disable,2.5V~5V:Dnable)	
13	PWM	Duty :10%~100%	
14	NC	No connection	

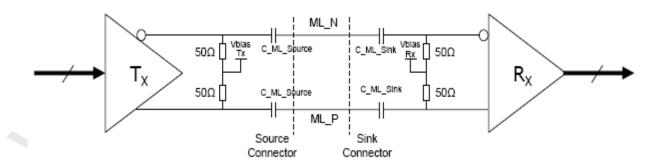
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5.3.eDP Interface



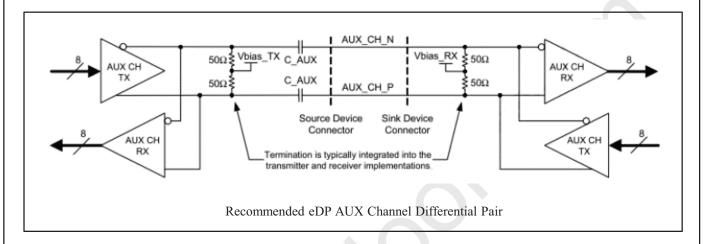
Note. Transmitter: Parade DP501or equivalent.

Transmitter is not contained in Module.



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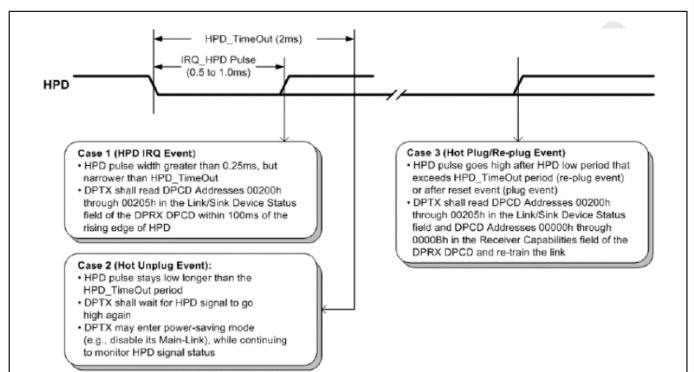
5.4 eDP AUX Channel Signal



Parameter	Symbol	Min	Тур	Max	Unit	Notes
AUX Unit Interval	UI	0.4	-	0.6	us	
AUX Jitter at Tx IC Package Pins		-	-	0.04	UI	
AUX Jitter at Rx IC Package Pins	T_{jitter}	-	-	0.05	UI	
AUX Peak-to-peak voltage at Connector Pins of Receiving		0.27	-	1.36	V	
AUX Peak-to-peak voltage at Connector Pins of Transmitting	V _{AUX-DIFFP-P}	0.29	-	1.38	V	
AUX EYE Width at Connector Pins of Tx and Rx		0.98	-	-	UI	
AUV DC	V _{AUX-CM_RX}	0	-	1.2	V	
AUX DC common mode voltage	V _{AUX-CM_TX}	0	-	1.2	V	
AUX AC Coupling Capacitor	C _{SOURCE-AUX}	75	-	200	nF	

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5.5 eDP HPD Signal



Parameter	Symbol	Min	Тур	Max	Unit	Notes
HPD Voltage		2.25	-	3.6	V	Sink side Driving
HOT Plug Detection Threshold	HPD	2.0	-	-	V	Source side Detecti
HOT Unplug Detection Threshold		-	-	0.8	V	ng
HPD_IRQ Pulse Width	HPD_IRQ	0.5	-	1.0	ms	
HPD_TimeOut		2.0	-	-	ms	HPD Unplug Event



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6.0 SIGNAL TIMING SPECIFICATION

6.1 The EV270QUM-N10 is operated by the DE only.

	Item	Symbol	Min	Тур	Max	Unit	Note
Period		tCLK	1.8	1.9	2.2	ns	
DCLK	Frequency	fCLK	444	533	551	MHz	
-	Period	tHP	3950	4000	4088	CLV	
Hsync	Width-Active	tWH	18	28	36	tCLK	
	Period	tVP	2213	2222	2290	tHP	
Vsync	Frequency	fv	50	60	62	HZ	Adaptive Sync :40~60Hz
	Width-Active	tWV	6	8	12	tHP	
	Horizontal valld	tHV	3840	3840	3840		
	Horizontal Back Porch	tHBP	32	54	112		
	Horizontal Front Porch	tHFP	60	78	100	tCLK	
Data	Horizontal Blank	-	110	160	248		tWH+tHBP+tH FP
Eenlabe	Vertial valld	tVV	2160	2160	2160		
	Vertial Back Porch	tVBP	5	7	18		
N	Vertial Front Porch	tvfp	42	47	100	tHP	
	Vertial Blank	-	53	62	130		twv+tvbp +tvfp

Note:

1. This panel supports adaptive sync timing ($40\sim60$ Hz) only under moving picture in room temperature (25 ± 5 °C).

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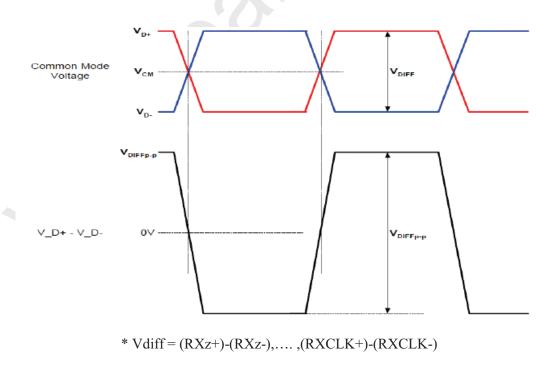
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6.2 eDP Rx Interface Timing Parameter

The specification of the eDP Rx interface timing parameter is shown in Table 7.

<Table 7. eDP Rx Interface Timing Specification>

Item	Symbol	Min	Тур	Max	Unit	Remark
Spread spectrum clock	ssc	0	-	0.5	%	
Differential peak-to-peak input voltage at package pins	VRX-DIFFp-p	120	<u>-</u> 1	_	mV	
Rx input DC common mode voltage	VRX_DC_CM	-	GND	<u>-</u>	V	
Differential termination resistance	RRX-DIFF	80	-	100	Ω	
Single-ended termination resistance	RRX-SE	40	-	60	Ω	
Rx short circuit current limit	IRX_SHORT		-	20	mA	
Intra-pair skew at Rx package pins (HBR) RX intra-pair skew tolerance at HBR	LRX_SKEW_ INTRA_PAIR	-	-	150	ps	





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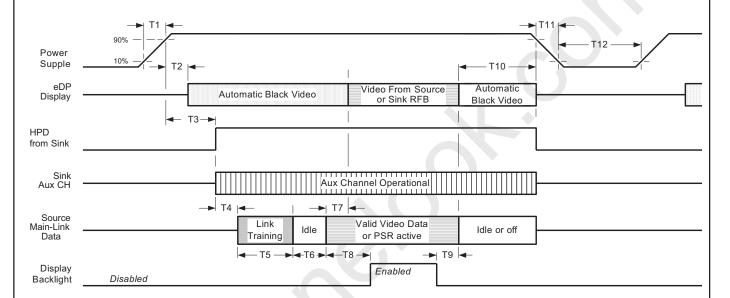
7.0 INPUT SIGNALS, BASIC DISPLAY COLORS & GRAY SCALE OF COLORS

				RI	ED I	DAT	ΓA				(GRI	EEN	I DA	ATA	Λ				BL	UE	DA	TA		\
Color & Gray Scale		R7	R6		R4			R1	R0	G7	G6	G5	G4		G2	G1	G0	В7	В6	В5	B4		B2	В1	В0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Green	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
D : G 1	Cyan	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
Basic Colors	Red	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Magenta	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Δ	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Darker	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Gray Scale	Δ					1								1								1			
of RED	∇					ļ							1.	<u> </u>								ļ			
	Brighter	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	∇	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Δ	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0
Gray Scale	Darker	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0
of GREEN	Δ					\uparrow							,	^								1			
OI OKEEN	∇					\downarrow								ļ								Į			
	Brighter	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0
	∇	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0
	Green	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Δ	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
Gray Scale	Darker	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
of BLUE	Δ					<u> </u>							•	<u> </u>								<u> </u>			
OI BLUE	∇																								
	Brighter	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1
	∇	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Δ	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	1
Gray Scale	Darker	0	0	0	0	0	0	1	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	1	0
of WHITE	Δ										<u> </u>								<u> </u>						
OI WILLE	∇		_			_		_	_	<u> </u>						_	_		<u> </u>	_	_				
	Brighter	1	1	1	1	1	1	0	1	1	1	1	1	1	1	0	1	1	1	1	1	1	1	0	1
	∇	1	1	1	1	1	1	1	0	1	1	1	1	1	1	1	0	1	1	1	1	1	1	1	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1

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8.0 POWER SEQUENCE

To prevent a latch-up or DC operation of the LCD module, the power on/off sequence shall be as shown in below



Timing Par ameter	Description	Required B	Lin	nits	Notes						
		у	Min	Max							
T1	Power rail rise time, 10% to 90%	Source	0.5ms	10ms							
T2	Delay from Power Sup ple to automatic Black Video generation		0ms	200ms	Automatic Black Video generation prevents display noise until valid video data is received from the Source						
Т3	Delay from Power Sup ple to HPD high	Sink	0ms	200ms	Sink AUX Channel must be operational upon HPD high						
T4	Delay from HPD high to link training initiali zation	Source	-	-	Allows for the Source to read Link capability and initialize						
T5	Link training duration	Source	-	-	Dependant on the Source link training protocol						
Т6	Link idle	Source	-	-	Min accounts for required BS-Idle Pattern. Max allows for S ource frame synchronization.						

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8.0 POWER SEQUENCE

	Т7	Delay from valid vide o data from Source to video on display	Sink	0ms	50ms	Max value allows for the Sink to validate video data and timing. At the end of T7, the Sink will indicate the detection of valid video data by setting the SINK_STATUS bit to logic 1 (DPCD 00205h, bit 0), and the Sink will no longer generate automatic Black Video.
	Т8	Delay from valid vide o data from Source to backlight enable	Source	-	-	The Source must assure display video is stable
	Т9	Delay from backlight disable to end of valid video data	Source	-	-	The Source must assure backlight is no longer illuminated. At the end of T9, the Sink will indicate the detection of no v alid video data by setting the SINK_STATUS bit to logic 0 (DPCD 00205h, bit 0), and the Sink will automatically displ ay Black Video.
	T10	Delay from end of vali d video data from Sour ce to power off		0ms	500ms	
	T11	Power rail fall time, 90% to 10%	Source		10ms	
Ī	T12	Power off time	Source	500ms	-	

Notes:

- 1. When the power supply VDD is 0V, keep the level of input signals on the low or keep high impedance.
- 2. Do not keep the interface signal high impedance when power is on.
- 3. Back Light must be turn on after power for logic and interface signal are valid.



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9.0 MECHANICAL CHARACTERISTICS

9.1 Dimensional Requirements

FIGURE 6 (located in Appendix) shows mechanical outlines for the model EV270QUM-N10. Other parameters are shown in Table 8.

<Table 8. Dimensional Parameters>

Parameter	Specification	Unit
Dimensional outline	$630.0(H) \times 368.2(V) \times 20.98(Depth)$	mm
Weight	3340 (typical)	gram
Active area	596.736(H) × 335.664 (V)	mm
Pixel pitch	$0.1554 \text{ (H)} \times 0.1554 \text{ (V)}$	mm
Number of pixels	$3840(H) \times 2160 (V) (1 \text{ pixel} = R + G + B \text{ dots})$	pixels
Back-light	Long Side, 2-LED Lighting Bar type	

9.2 Anti-Glare and Polarizer Hardness.

The surface of the LCD has an anti-glare coating to minimize reflection and a coating to reduce scratching.

9.3 Light Leakage

There shall not be visible light from the back-lighting system around the edges of the screen as seen from a distance 50cm from the screen with an overhead light level of 350lux.



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10.0 RELIABLITY TEST

The Reliability test items and its conditions are shown in below. <Table 9 Reliability Test Parameters >

No	Test Items	Conditions			
1	High temperature storage test	Ta = 60 °C, 240 hrs			
2	Low temperature storage test	Ta = -20 °C, 240	hrs		
3	High temperature & high humidity operation test	Ta = 50 °C, 80%	RH, 240hrs		
4	High temperature operation test	Ta = 50 °C, 240h	rs		
5	Low temperature operation test	Ta = 0°C, 240hrs			
6	Thermal shock	$Ta = -20 \text{ °C} \leftrightarrow 60$	0 °C (0.5 hr), 100 cycle		
7	Packing Vibration test (non-operating)	Frequency Gravity / AMP Period	Random,1 ~ 200 Hz, 30 min/Axis 1.2 Grms X, Y, Z 30 min		
		Gravity	50G		
8	Shock test (non-operating)	Pulse width	11msec, Half sine wave		
		Direction	±X, ±Y, ±Z Once for each		
9	Electro-static discharge test	Air : 150 pF Contact : 150 pF	F, 330Ω, 15 KV F, 330Ω, 8 KV		

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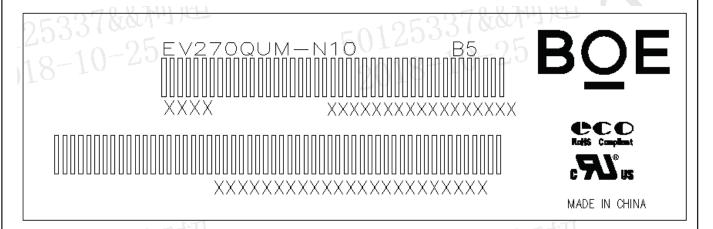
11.0 HANDLING & CAUTIONS

- (1) Cautions when taking out the module
 - Pick the pouch only, when taking out module from a shipping package.
- (2) Cautions for handling the module
 - As the electrostatic discharges may break the LCD module, handle the LCD module with care. Peel a protection sheet off from the LCD panel surface as slowly as possible.
 - As the LCD panel and back light element are made from fragile glass material, impulse and pressure to the LCD module should be avoided.
 - As the surface of the polarizer is very soft and easily scratched, use a soft dry cloth without chemicals for cleaning.
 - Do not pull the interface connector in or out while the LCD module is operating.
 - Put the module display side down on a flat horizontal plane.
 - Handle connectors and cables with care.
- (3) Cautions for the operation
 - When the module is operating, do not lose CLK, ENAB signals. If any one of these signals is lost, the LCD panel would be damaged.
 - Obey the supply voltage sequence. If wrong sequence is applied, the module would be damaged.
- (4) Cautions for the atmosphere
 - Dew drop atmosphere should be avoided.
 - Do not store and/or operate the LCD module in a high temperature and/or humidity atmosphere. Storage in an electro-conductive polymer packing pouch and under relatively low temperature atmosphere is recommended.
- (5) Cautions for the module characteristics
 - Do not apply fixed pattern data signal to the LCD module at product aging.
 - Applying fixed pattern for a long time may cause image sticking.
- (6) Other cautions
 - Do not disassemble and/or re-assemble LCD module.
 - Do not re-adjust variable resistor or switch etc.
 - •When returning the module for repair or etc., Please pack the module not to be broken. We recommend to use the original shipping packages.



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12.0 PRODUCT SERIAL NUMBER



MDL ID Naming Rule:

Digit Code	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17
Code	s	L	s	5	1	2	3	5	9	4	2	0	0	0	1	D	В
Description	CALL TO SERVICE OF	l Code BN	Grad e	Line	Y	ear	Mont h			ension its Of Fo	Code GCOD)	Serial No 00001-ZZZZZZ					

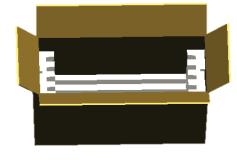
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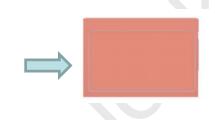
13.0 Packing

13.1 Packing Order

1.Put Bottom into the box

- 2.-Put Protection Film on Panel , Put MDL in PE Bag -Put MDL in groove in order, keeping Panel side the same with arrow , Put 1ea Cover on top of Bottom
 - -Capacity:5pcs Panel/Inner Box









厢车装载方式:双层码放

Pallet摆放方式:一横一竖

12m厢车装载量: 2640Pcs (44托)

- 4.-Put the Pallet into Truck with 3 rows and 2 layers
 - -Capacity:44EA Pallet/Truck , 2460pcs Panel/Truck
- 3.-Put 6EA Box on surface of Pallet, pile 2 layers in total.
 - -Use 6 Paper Corner to protect, Strapping tapes to fix and wrap film to package the Boxes
 - -Capacity: 6EA Box/layer;2 layers in total;60pcs/ Pallet

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13.2 Packing Note

• Box Dimension : 714mm(L) × 272mm(W) × 472mm(H)

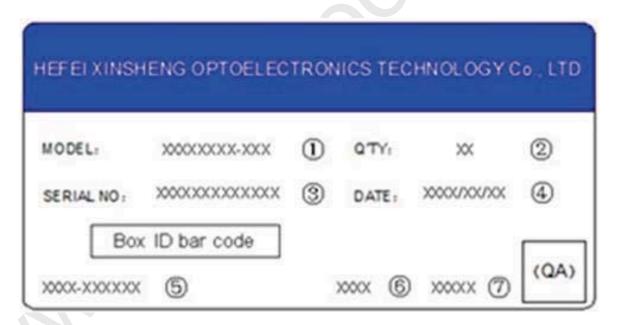
• Package Quantity in one Box : 5pcs

13.3 Box label

• Label Size : 110 mm (L) × 55mm (W)

Contents

Model: EV270QUM-N10 Q'ty: Module * Q'ty in one box Serial No.: Box Serial No. Date: Packing Date



The printed part follow as:

- 1. FG-CODE
- 3. Box ID
- 4. Customer Code
- **8.** FG-CODE(the last four number)
- 7. Vendor Code

- 2. Quantity
- 4. Packing Date

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14.0 APPENDIX

Figure 1. Measurement Set Up

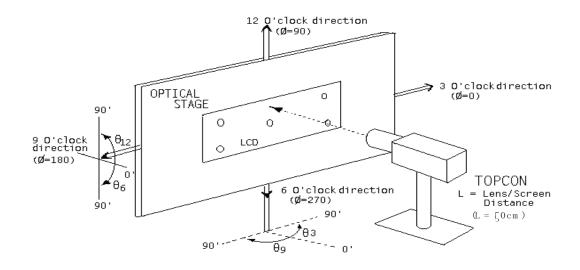
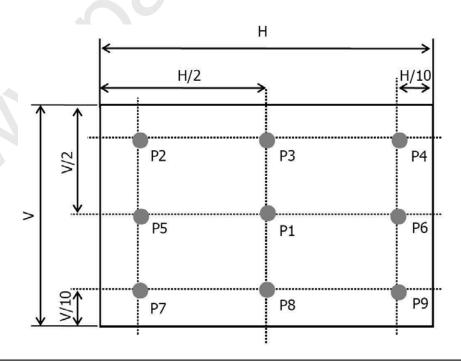


Figure 2. White Luminance and Uniformity Measurement Locations (9 points)

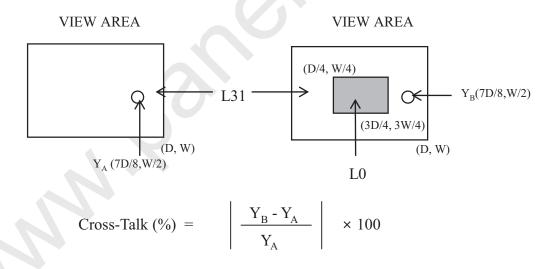


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Figure 3. Response Time Testing



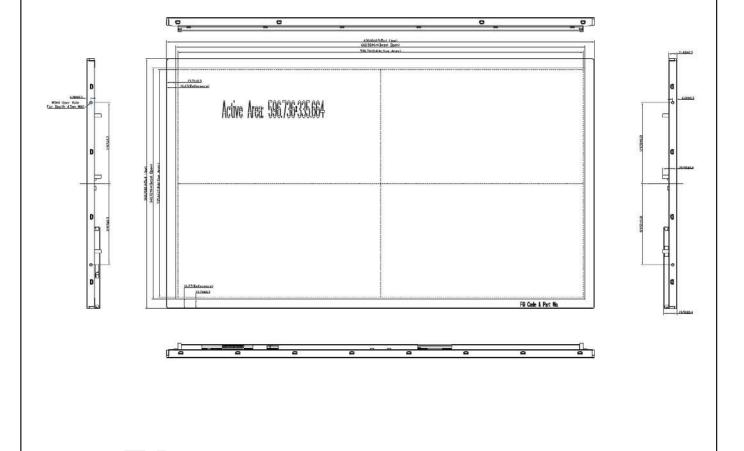
Figure 4. Cross Modulation Test Description



 $\begin{array}{ccc} Where: & Y_A = Initial \ luminance \ of \ measured \ area \ (cd/m^2) \\ & Y_B = Subsequent \ luminance \ of \ measured \ area \ (cd/m^2) \\ The \ location \ measured \ will \ be \ exactly \ the \ same \ in \ both \ patterns \\ \end{array}$

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 $Figure \ 5. \ TFT\text{-}LCD \ Module \ Outline \ Dimensions \ (Front \ \& \ Side \ \ view)$



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Figure 6. TFT-LCD Module Outline Dimensions (Rear view)

