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Issued Date: Jan. 11, 2010 Model No.: G121S1-L02

# **TFT LCD Approval Specification**

# **MODEL NO.: G121S1-L02**

Customer:
Approved by:
Note:

Liquid Crystal E	Liquid Crystal Display Division					
QRA Division.	APPL Head Division.					
Approval	Approval					

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# **REVISION HISTORY**

2.0       Jan. 11, 2010       All       G121S1-L02 Approval Spec. was first issued.         G121S1-L02 Approval Spec.       G121S1-L02 Approval Spec.       G121S1-L02 Approval Spec.	Version	Date	Section	Description
			All	G121S1-L02 Approval Spec. was first issued.

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# **1. GENERAL DESCRIPTION**

## 1.1 OVERVIEW

The G121S1-L02 model is a 12.1" TFT-LCD module with a white LED Backlight Unit and a 20-pin 1ch-LVDS interface. This module supports 800 x 600 SVGA MVA mode and displays 262K/ 16.2M colors. The converter for the LED Backlight Unit is built in.

## **1.2 FEATURES**

- Wide viewing angle
- High contrast ratio
- SVGA (800 x 600 pixels) resolution
- Wide operating temperature
- DE (Data Enable) mode
- LVDS (Low Voltage Differential Signaling) interface
- Reversible-scan direction
- RoHS Compliance
- LED Light Bar Replaceable

#### **1.3 APPLICATION**

- TFT LCD Monitor
- Industrial Application
- Amusement

### 1.4 GENERAL SPECIFICATIONS

Item	Specification	Unit	Note
Diagonal Size	12.1	inch	
Active Area	246.00(H) x 184.50(V)	mm	(1)
Bezel Opening Area	249.00(H) x 187.50(V)	mm	
Driver Element	a-si TFT active matrix	-	-
Pixel Number	800 x R.G.B. x 600	pixel	-
Pixel Pitch	0.3075(H) x 0.3075(V)	mm	-
Pixel Arrangement	RGB vertical stripe	-	-
Display Colors	262K/ 16.2M	color	-
Transmissive Mode	Normally black	-	-
Surface Treatment	Hard coating (3H), Anti-glare (Haze 25%)	-	-
Module Power Consumption	13.52	W	(3),Typ.

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#### **1.5 MECHANICAL SPECIFICATIONS**

Item		Min.	Тур.	Max.	Unit	Note
	Horizontal (H)	260.0	260.5	261.0	mm	
Module Size	Vertical (V)	203.5	204.0	204.5	mm	(1)
	Depth (D)	7.9	8.4	8.9	mm	
Weight			520	550	g	-
I/F connector mounting		The mounting ir		(2)		
pos	sition	the screen cente	er within ±0.5mm a	as the horizontal.	-	(2)

Note (1) Please refer to the attached drawings for more information of front and back outline dimensions.

(2) Connector mounting position



(3) The Module Power Consumption is specified at 3.3V, white pattern and 100% duty for LED backlight.

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# 2. ABSOLUTE MAXIMUM RATINGS

#### 2.1 ABSOLUTE RATINGS OF ENVIRONMENT

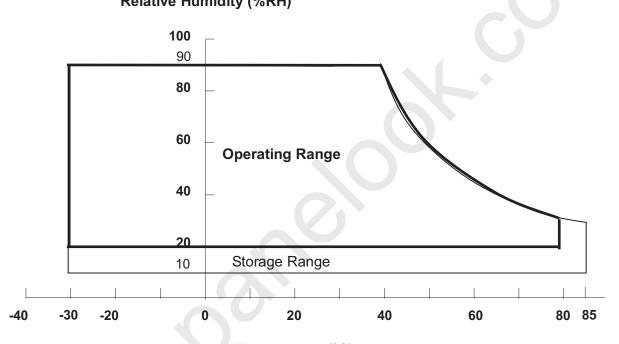
Item	Svmbol	Va	lue	Unit	Note
Item	Symbol	Min.	Max.	Unit	NOLE
Operating Ambient Temperature	T <sub>OP</sub>	-30	+80	°C	
Storage Temperature	T <sub>ST</sub>	-30	+85	°C	

Note (1) Temperature and relative humidity range is shown in the figure below.

(2) 90 %RH Max. (Ta  $\leq$  40 °C).

(3) Wet-bulb temperature should be 39 °C Max. (Ta > 40 °C).

(4) No condensation.



# **Relative Humidity (%RH)**

Temperature (°C)

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### 2.2 ELECTRICAL ABSOLUTE RATINGS

#### 2.2.1 TFT LCD MODULE

ltom	Symbol	Value		Linit	Noto	
Item	Symbol	Min.	Max.	Unit	Note	
Power Supply Voltage	VCC	-0.3	7	V	(1)	

#### 2.2.2 LED CONVERTER

Item	tem Symbol Value		Unit	Note	
liem	Symbol	Min.	Max.	Unit	Note
Converter Voltage	Vi	-0.3	18	V	(1) , (2)
Enable Voltage	EN		5.5	V	
Backlight Adjust	ADJ		5.5	V	

Note (1) Permanent damage to the device may occur if maximum values are exceeded. Function operation should be restricted to the conditions described under Normal Operating Conditions.

Note (2) Specified values are for lamp (Refer to 3.2 for further information).

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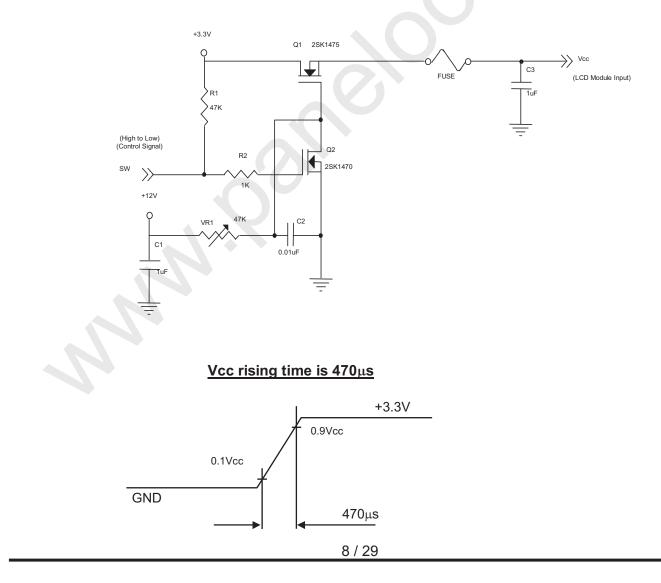
**3. ELECTRICAL CHARACTERISTICS** 

# 3.1 TFT LCD MODULE

.1 TFT LCD MODULE							Ta = 25 ± 2 °C	
Parameter	Parameter			Value		Unit	Note	
		Symbol	Min.	Тур.	Max.	Onit	Note	
Power Supply Voltage		vcc	3.0	3.3	3.6	V	at VCC=3.3V	
i ower oupply voltage	rower Suppry voltage		4.75	5.0	5.25	V	at VCC=5.0V	
Rush Current	Rush Current		-	-	1.5	Α	(2), at VCC=5.0V	
	White	/bito		460		mA	(3)a, at VCC=3.3V, 60Hz	
Power Supply Current	vvnite	VVIIICE			310		mA	(3)a, at VCC=5.0V, 60Hz
	Black			420		mA	(3)b, at VCC=3.3V, 60Hz	
	DIACK			280		mA	(3)b, at VCC=5.0V, 60Hz	
Power Consumption		PL		1.52		W	VCC=3.3V, 60Hz, White Pattern	
LVDS differential input voltage		VID	100		600	mV		
LVDS common input vo	Itage	VICM	0.7		1.6	V	-	

Note (1) The module is recommended to operate within specification ranges listed above for normal function.

Note (2) Measurement Conditions:





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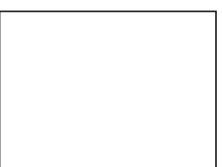


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- Note (3) The specified power supply current is under the conditions at Ta =  $25 \pm 2$  °C,  $f_v = 60$  Hz, whereas a power dissipation check pattern below is displayed.
  - a. White Pattern

b. Black Pattern



Active Area



Active Area

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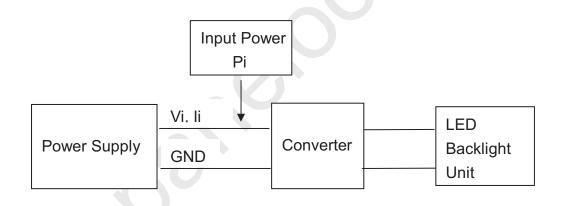
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## 321 ED CONVERTER

3.2 LED CONVERTER						Ta =	25 ± 2 °C
Paramet	Symbol		Value			Note	
1 alamet	.61	Cymbol	Min.	Тур.	Max.	Unit	NOLE
Converter Power Supply	Voltage	Vi	10.8	12.0	13.2	V	(Duty 100%)
Converter Power Supply	Current	I.		1.0		А	@ Vi = 12V
Converter i ower Cuppiy	ourrent	li		1.0		~	(Duty 100%)
Converter Power Consum	ntion	Pi		12		w	@ Vi = 12V
Converter i ower Consult	iption			12		~ ~	(Duty 100%)
EN Control Level	Backlight on		2.0	3.3	5.0	V	
	Backlight off		0		0.8	V	
PWM Control Level	PWM High Level		2.0	3.3	5.0	V	
	PWM Low Level		0		0.15	V	
PWM Control Duty Ratio		20		100	%		
PWM Control Frequency	f <sub>PWM</sub>	190	200	210	Hz		
LED Life Time		L	50,000			Hrs	(2)

Note (1) LED current is measured by utilizing a high frequency current meter as shown below:

Note (2) The lifetime of LED is defined as the time when it continues to operate under the conditions at Ta = 25 ±2  $^\circ$ C and I<sub>LED</sub> = 80mA<sub>DC</sub> (LED forward current) until the brightness becomes  $\leq$  50% of its original value.



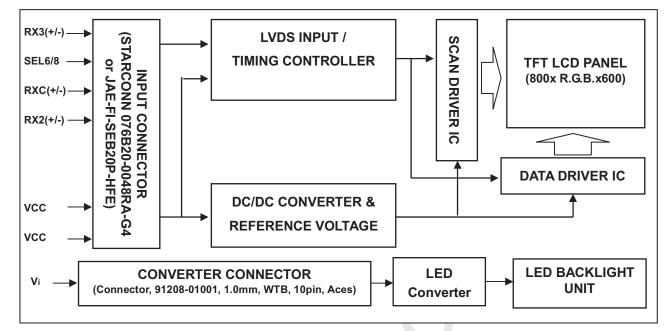


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4. BLOCK DIAGRAM

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4.1 TFT LCD MODULE





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## 5. INPUT TERMINAL PIN ASSIGNMENT

#### 5.1 TFT LCD MODULE

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Pin	Name	Description	Remark
1	RX3+	Differential Data Input, CH3 (Positive)	
2	RX3-	Differential Data Input, CH3 (Negative)	
3	GND	GND	
		LVDS 6/8 bit select function control,	Note (3)
4	SEL68	Low or NC $\rightarrow$ 6 bit Input Mode	
		High $\rightarrow$ 8bit Input Mode	
5	GND	Ground	
6	RXC+	Differential Clock Input (Positive)	LVDS Level Clock
7	RXC-	Differential Clock Input (Negative)	
8	GND	Ground	
9	RX2+	Differential Data Input, CH2 (Positive)	
10	RX2-	Differential Data Input, CH2 (Negative)	
11	GND	Ground	
12	RX1+	Differential Data Input, CH1 (Positive)	
13	RX1-	Differential Data Input, CH1 (Negative)	
14	GND	Ground	
15	RX0+	Differential Data Input, CH0 (Positive)	
16	RX0-	Differential Data Input, CH0 (Negative)	
		Horizontal Reverse Scan Control,	Note (3)
17	reLR	Low or NC $\rightarrow$ Normal Mode.	
		High → Horizontal Reverse Scan	
		Vertical Reverse Scan Control,	Note (3)
18	reUD	Low or NC $\rightarrow$ Normal Mode,	
		High → Vertical Reverse Scan	
19	VCC	Power supply	
20	VCC	Power supply	

Note (1) Connector Part No.: FI-SEB20P-HFE(JAE) or 076B20-0048RA-G4(STARCONN) or equivalent.

Note (2) User's connector Part No.: FI-SE20ME(JAE) or equivalent

Note (3) "Low" stands for 0V. "High" stands for 3.3V. "NC" stands for "No Connected".

#### 5.2 LED CONVERTER

Pin	Symbol	Description	Remark
1	Vi	Converter input voltage	12V
2	Vi	Converter input voltage	12V
3	Vi	Converter input voltage	12V
4	Vi	Converter input voltage	12V
5	V <sub>GND</sub>	Converter ground	Ground
6	V <sub>GND</sub>	Converter ground	Ground
7	V <sub>GND</sub>	Converter ground	Ground
8	V <sub>GND</sub>	Converter ground	Ground
9	EN	Enable pin	3.3V
10			PWM Dimming
	ADJ	Backlight Adjust	(190-210Hz, Hi: 3.3V <sub>DC</sub> ,
			Lo: 0V <sub>DC</sub> )

Note (1) Connector Part No.: 91208-01001-H01(ACES) or equivalent

Note (2) User's connector Part No.: 91209-01011(ACES) or equivalent

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#### 5.3 COLOR DATA INPUT ASSIGNMENT

The brightness of each primary color (red, green and blue) is based on the 6-bit gray scale data input for the color. The higher the binary input, the brighter the color. The table below provides the assignment of color versus data input.

									D	)ata S		al		1					
	Color			R						Gre						Bl			
		R5	R4	R3	R2	R1	R0	G5	G4	G3	G2	G1	G0	B5	B4	B3	B2	B1	B0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Green	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
Basic	Blue	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1
Colors	Cyan	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Red(0)/Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(1)	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
Gray	Red(2)	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0
Scale	:	:	:	:	:	:	:	:	:	:	:	1	:	•:	:	:	:	:	:
Of	:	:	:	:	:	:	:	:	:	:	:			:	:	:	:	:	:
Red	Red(61)	1	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0
	Red(62)	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(63)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Green(0)/Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green(1)	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
Gray	Green(2)	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0
Scale	:	:	:	:	:	:	:		:	:	:	:	:	:	:	:	:	:	:
Of	:	:	:	:	:	:	:	: )	:	:	:	:	:	:	:	:	:	:	:
Green	Green(61)	0	0	0	0	0	0	1	1	1	1	0	1	0	0	0	0	0	0
	Green(62)	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0	0
	Green(63)	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
	Blue(0)/Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue(1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
Gray	Blue(2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
Scale	:	:		:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
Of	:	:		:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
Blue	Blue(61)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	0	1
	Blue(62)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	0
	Blue(63)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1

Note (1) 0: Low Level Voltage, 1: High Level Voltage

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The brightness of each primary color (red, green and blue) is based on the 8-bit gray scale data input for the color. The higher the binary input, the brighter the color. The table below provides the assignment of color versus data input.

													Data	ı Siç	gnal										
	Color	Red				Green					Blue														
		R7	R6	R5	R4	R3	R2	R1	R0	G7	G6	G5	G4	G3	G2	G1	G0	В7	B6	B5	B4	В3	B2	B1	В0
Basic Colors	Black Red Green Blue Cyan Magenta Yellow White	0 1 0 0 1 1 1	0 1 0 0 1 1	0 1 0 0 1 1 1	0 1 0 0 1 1	0 1 0 0 1 1 1	0 1 0 0 1 1	0 1 0 0 1 1	0 1 0 0 1 1 1	0 0 1 0 1 1	0 0 1 0 1 0 1	0 0 1 0 1 0 1	0 0 1 0 1 1	0 0 1 0 1 0 1	0 0 1 0 1 1 1	0 0 1 0 1 1	0 0 1 0 1 1	0 0 1 1 0 1	0 0 1 1 1 0	0 0 1 1 1 0	0 0 1 1 1 0 1	0 0 1 1 1 0 1	0 0 1 1 1 0 1	0 0 1 1 1 0 1	0 0 1 1 1 0 1
Gray Scale Of Red	Red(0) / Dark Red(1) Red(2) : Red(253) Red(254) Red(255)	0 0 : 1 1	0 0 : 1 1	0 0 : 1 1	0 0 : 1 1	0 0 : 1 1	0 0 : 1 1	0 0 1 : 0 1	0 1 0 : 1 0 1	0 0 : : 0 0	0 0 0 : : 0 0 0	0 0 0 : : 0 0 0	0 0 : : 0 0	0 0 : : 0 0 0	000000	0 0 : : 0 0 0	0 0 : : 0 0 0	0 0 : : 0 0 0	0 0 : : 0 0	0 0 : : 0 0 0	0 0 : : 0 0	0 0 : : 0 0	0 0 : : 0 0 0	0 0 : : 0 0	0 0 : : 0 0
Gray Scale Of Green	Green(0)/ Dark Green(1) Green(2) : Green(253) Green(254) Green(255)	0 0 : : 0 0	0 0 : : 0 0 0	0 0 0 : : 0 0 0	0 0 0 : : 0 0 0	000:000	0000000	0 0 0 : : 0 0 0	0 0 0 : : 0 0 0	0 0 : : 1 1	0 0 0 : : 1 1	0 0 0 : 1 1	0 0 : : 1 1	0 0 : : 1 1	0 0 : : 1 1	0 0 1 : 0 1	0 1 0 : 1 0	0 0 : : 0 0	0 0 : : 0 0 0	0 0 : : 0 0 0	0 0 : : 0 0 0	0 0 : : 0 0 0	0 0 : : 0 0 0	0 0 : : 0 0	0 0 : : 0 0
Gray Scale Of Blue	Blue(0) / Dark Blue(1) Blue(2)  Blue(253) Blue(254) Blue(255)	0 0 0 : : 0 0 0	0 0 0 : : 0 0 0	0 0 : : 0 0	0 0 : : 0 0	0 0 : : 0 0	0 0 : : 0 0 0	0 0 : : 0 0	0 0 : : 0 0	0 0 : : 0 0	0 0 : : 0 0	0 0 : : 0 0	0 0 : 0 0 0	0 0 : : 0 0	0 0 : 0 0 0	0 0 : 0 0 0	0 0 : : 0 0 0	0 0 : 1 1	0 0 : 1 1	0 0 : 1 1	0 0 : 1 1	0 0 : 1 1	0 0 : : 1 1	0 0 1 : 0 1	0 1 : : 1 0 1

Note: 0: Low Level Voltage, 1: High Level Voltage



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# 6. INTERFACE TIMING

#### **6.1 INPUT SIGNAL TIMING SPECIFICATIONS**

The input signal timing specifications are shown as the following table and timing diagram.

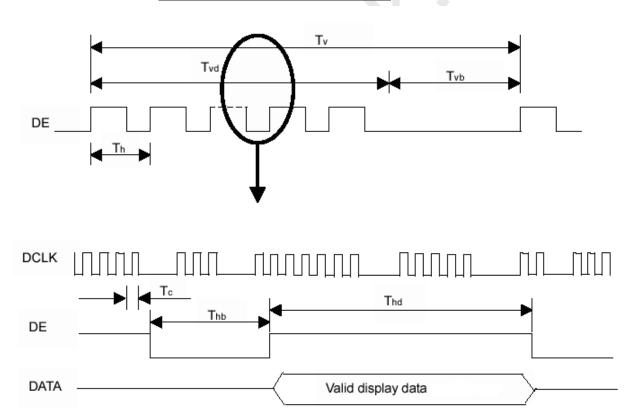
Signal	Item	Symbol	Min.	Тур.	Max.	Unit	Note
DCLK	Frequency	Fc	34	40	48.3	MHz	
	Total	Τv	610	628	800	Th	Tv=Tvd+Tvb
Vertical Active Display Term	Display	Tvd		600		Th	
	Blank	Tvb	Tv-Tvd	28	Tv-Tvd	Th	
	Total	Th	960	1056	1150	Tc	Th=Thd+Thb
Horizontal Active Display Term	Display	Thd		800		Tc	
	Blank	Thb	Th-Thd	256	Th-Thd	Тс	

Note : (1) Because this module is operated by DE only mode, Hsync and Vsync input signals should be

set to low logic level or ground. Otherwise, this module would operate abnormally.

(2) Frame rate is 60Hz

## INPUT SIGNAL TIMING DIAGRAM



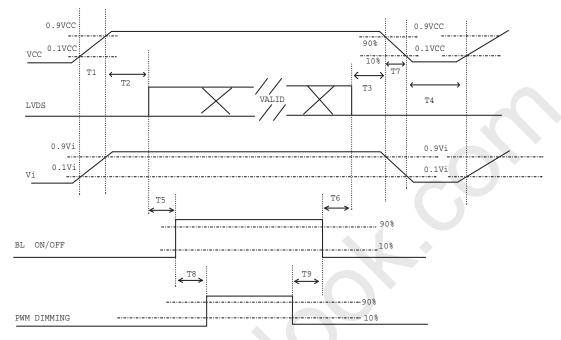


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### 6.2 POWER ON/OFF SEQUENCE

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To prevent a latch-up or DC operation of LCD module, the power on/off sequence should follow the conditions shown in the following diagram.



#### **Power ON/OFF sequence**

- Note (1) Please avoid floating state of interface signal at invalid period.
- Note (2) When the interface signal is invalid, be sure to pull down the power supply of LCD VCC to 0 V.
- Note (3) The Backlight converter power must be turned on after the power supply for the logic and the interface signal is valid. The Backlight converter power must be turned off before the power supply for the logic and the interface signal is invalid.

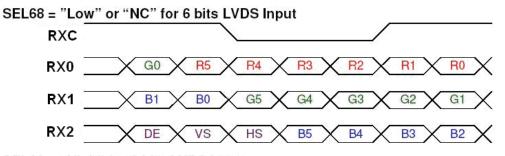
Parameter			Units	
rarameter	Min	Тур	Max	Units
T1	0.5		10	ms
T2	0		50	ms
Т3	0		50	ms
T4	500			ms
Т5	200			ms
Т6	200			ms
Τ7	5		300	ms
<b>T8</b>	10			ms
Т9	10			ms



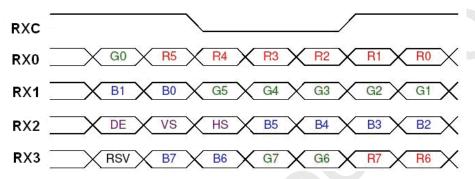
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SEL68 = "High" for 8 bits LVDS Input



Note (1) R/G/B data 7: MSB, R/G/B data 0: LSB

Note (2) Please follow PSWG

Signal Name	Description	Remark
R7	Red Data 7 (MSB)	Red-pixel Data
R6	Red Data 6	Each red pixel's brightness data consists of these
R5	Red Data 5	8 bits pixel data.
R4	Red Data 4	
R3	Red Data 3	
R2	Red Data 2	
R1	Red Data 1	
R0	Red Data 0 (LSB)	
G7	Green Data 7 (MSB)	Green-pixel Data
G6	GreenData 6	Each green pixel's brightness data consists of these
G5	GreenData 5	8 bits pixel data.
G4	GreenData 4	
G3	GreenData 3	
G2	GreenData 2	
G1	GreenData 1	
G0	GreenData 0 (LSB)	
B7	Blue Data 7 (MSB)	Blue-pixel Data
B6	Blue Data 6	Each blue pixel's brightness data consists of these
B5	Blue Data 5	8 bits pixel data.
B4	Blue Data 4	
B3	Blue Data 3	
B2	Blue Data 2	
B1	Blue Data 1	
B0	Blue Data 0 (LSB)	
RXCLKIN+	LVDS Clock Input	
RXCLKIN-		
DE	Display Enable	
VS	Vertical Sync	
HS	Horizontal Sync	

Note (3) Output signals from any system shall be low or Hi-Z state when VCC is off

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# 6.4 SCANNING DIRECTION

The following figures show the image see from the front view. The arrow indicates the direction of scan.

Fig.1 Normal Scan



Fig.2 Reverse Scan



Fig.3 Reverse Scan

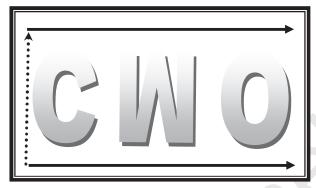




Fig. 1 Normal scan ( pin 17, reLR = Low or NC, pin 18, reUD = Low or NC )

Fig. 2 Reverse scan ( pin 17, reLR = High, pin 18, reUD = Low or NC )

- Fig. 3 Reverse scan ( pin 17, reLR = Low or NC, pin 18, reUD = High )
- Fig. 4 Reverse scan ( pin 17, reLR = High, pin 18, reUD = High )

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# 7. OPTICAL CHARACTERISTICS

#### 7.1 TEST CONDITIONS

Item	Symbol	Value	Unit						
Ambient Temperature	Та	25±2	O°						
Ambient Humidity	На	50±10	%RH						
Supply Voltage	V <sub>CC</sub>	3.3	V						
Input Signal	According to typical v	According to typical value in "3. ELECTRICAL CHARACTERISTICS"							
Converter Voltage	V <sub>in</sub>	12	V						
Converter Duty		100	%						

#### 7.2 OPTICAL SPECIFICATIONS

The relative measurement methods of optical characteristics are shown in 7.2. The following items should be measured under the test conditions described in 7.1 and stable environment shown in Note (5).

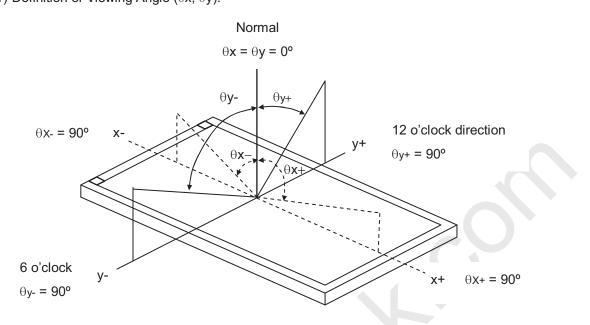
Item	ו	Symbol	Condition	Min.	Тур.	Max.	Unit	Note	
	Red	Rx			0.600		-		
	Keu	Ry			0.353		-		
	Green	Gx			0.348		-		
Color	Green	Gy		Тур -	0.568	Typ +	-	(1), (5)	
Chromaticity	Blue	Bx	θ <sub>x</sub> =0°, θ <sub>Y</sub> =0°	0.05	0.150	0.05	-	(1), (3)	
	Diue	By	CS-2000		0.097		-		
	White	Wx			0.313		-		
		Wy			0.329		-		
Center Luminan	ce of White	L <sub>C</sub>		500	600		-	(4), (5)	
Contrast Ratio		CR		1200	1500		-	(2), (5)	
Response Time		T <sub>R</sub>	θ <sub>x</sub> =0°, θ <sub>Y</sub> =0°	-	13	18	ms	(3)	
		T <sub>F</sub>	0 <sub>x</sub> =0 , 0 <sub>y</sub> =0	-	12	17	ms	(3)	
White Variation		δW	θ <sub>x</sub> =0°, θ <sub>Y</sub> =0°	-	1.25	1.4	-	(5), (6)	
	Horizoptol	θ <b>x</b> +		80	89	-			
	Horizontal	θ <b><sub>x</sub>-</b>		80	89	-	Dog	(1) (5)	
Viewing Angle	Vertical	θ <b>γ</b> +	CR≥10	80	89	-	Deg.	(1), (5)	
	Vertical	θγ-		80	89	-			

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Note (1) Definition of Viewing Angle ( $\theta x, \theta y$ ):



## Note (2) Definition of Contrast Ratio, CR:

The contrast ratio can be calculated by the following expression.

Contrast Ratio, CR = L63 (255) / L0

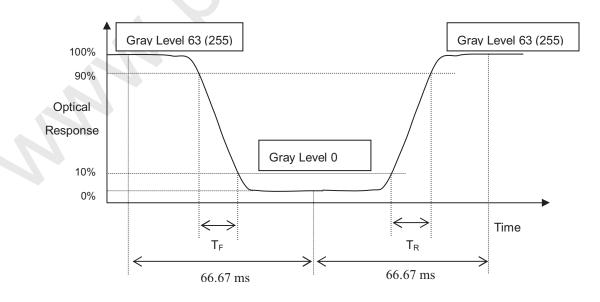
L63: Luminance of gray level 63 (255)

L 0: Luminance of gray level 0

CR = CR (5)

CR (X) is corresponding to the Contrast Ratio of the point X at Figure in Note (6).

Note (3) Definition of Response Time  $(T_R, T_F)$  and measurement method:



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Note (4) Definition of Luminance of White, L<sub>C</sub>:

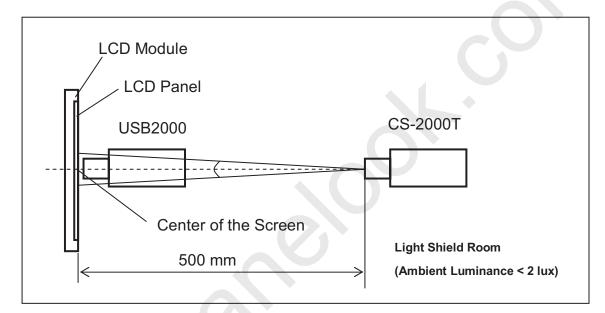
Measure the luminance of gray level 63 (255) at center point

 $L_{c} = L(5)$ 

L (x) is corresponding to the luminance of the point X at Figure in Note (6).

Note (5) Measurement Setup:

The LCD module should be stabilized at given temperature for 20 minutes to avoid abrupt temperature change during measuring. In order to stabilize the luminance, the measurement should be executed after lighting Backlight for 20 minutes in a windless room.



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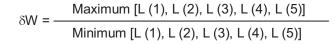


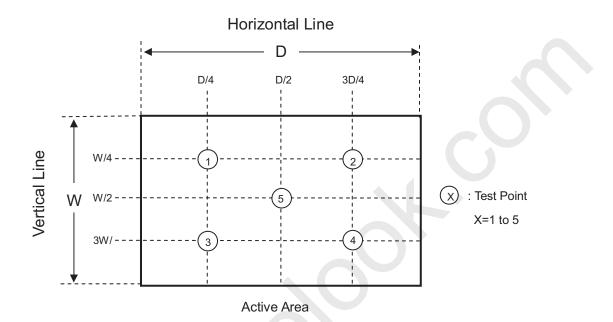
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Note (6) Definition of White Variation ( $\delta W$ ):

Measure the luminance of gray level 63 (255) at 5 points





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Test Item	Test Condition	Note
High Temperature Storage Test	85°C, 240 hours	
Low Temperature Storage Test	-30°C, 240 hours	
Thermal Shock Storage Test	-30°C, 0.5hour↔85°C, 0.5hour; 1hour/cycle,100cycles	
High Temperature Operation Test	80°C, 240 hours	(1)(2)
Low Temperature Operation Test	-30°C, 240 hours	
High Temperature & High Humidity Operation Test	60ºC, 90%RH, 240hours	
Shock (Non-Operating)	200G, 2ms, half sine wave, 1 time for ± X, ± Y, ± Z.	(3)
Vibration (Non-Operating)	1.5G, 10 ~ 300 Hz, 10min/cycle, 3 cycles each X, Y, Z	(3)

Note (1) There should be no condensation on the surface of panel during test.

Note (2) Temperature of panel display surface area should be 85 °C Max.

Note (3) At testing Vibration and Shock, the fixture in holding the module has to be hard and rigid enough so that the module would not be twisted or bent by the fixture.

Note (4) In the standard conditions, there is no function failure issue occurred. All the cosmetic specification

is judged before reliability test.

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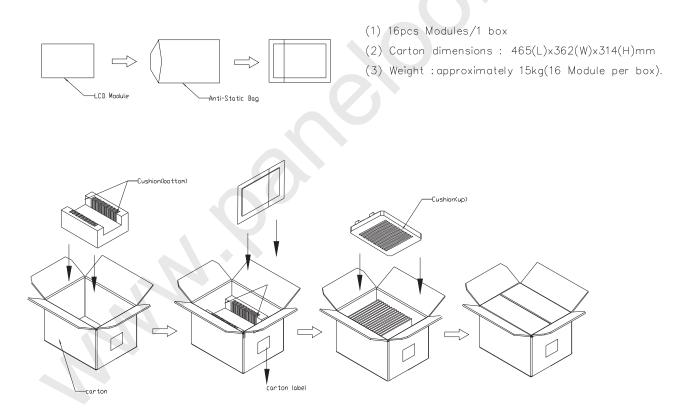
#### 9. PACKAGING

- 9.1 PACKING SPECIFICATIONS
  - (1) 16pcs LCD modules / 1 Box
  - (2) Box dimensions: 465 (L) X 362 (W) X 314 (H) mm
  - (3) Weight: approximately 15Kg (16 modules per box)

#### 9.2 PACKING METHOD

(1) Carton Packing should have no failure in the following reliability test items.

Test Conditions		Note
ISTA STANDARD		
Random, Frequency Range: 2 – 200 Hz		
Top & Bottom: 30 minutes (+Z), 10 min (-Z),		Non Operation
Right & Left: 10 minutes (X)		
Back & Forth 10 minutes (Y)		
1 Angle, 3 Edge, 6 Face, 61 cm		Non Operation
	ISTA STANDARD Random, Frequency Range: 2 – 200 Hz Top & Bottom: 30 minutes (+Z), 10 min (-Z), Right & Left: 10 minutes (X) Back & Forth 10 minutes (Y)	ISTA STANDARD Random, Frequency Range: 2 – 200 Hz Top & Bottom: 30 minutes (+Z), 10 min (-Z), Right & Left: 10 minutes (X) Back & Forth 10 minutes (Y)





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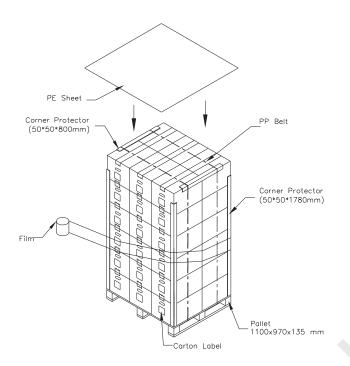


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Approval

Sea / Land Transportation (40ft Container)



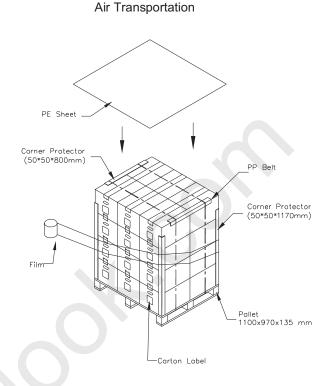


Figure. 9-2 Packing method

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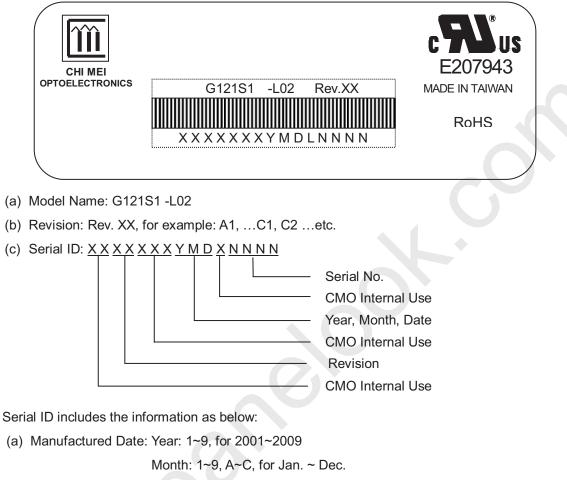


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# **10. DEFINITION OF LABELS**

#### 10.1 CMO MODULE LABEL

The barcode nameplate is pasted on each module as illustration, and its definitions are as following explanation.



Day: 1~9, A~Y, for 1<sup>st</sup> to 31<sup>st</sup>, exclude I, O and U

- (b) Revision Code: cover all the change
- (c) Serial No.: Manufacturing sequence of product



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### **11. PRECAUTIONS**

#### 11.1 ASSEMBLY AND HANDLING PRECAUTIONS

- (1) Do not apply rough force such as bending or twisting to the module during assembly.
- (2) To assemble or install module into user's system can be only in clean working areas. The dust and oil may cause electrical short or worsen the polarizer.
- (3) It's not permitted to have pressure or impulse on the module because the LCD panel and Backlight will be damaged.
- (4) Always follow the correct power sequence when LCD module is connecting and operating. This can prevent damage to the CMOS LSI chips during latch-up.
- (5) Do not pull the I/F connector in or out while the module is operating.
- (6) Do not disassemble the module.
- (7) Use a soft dry cloth without chemicals for cleaning, because the surface of polarizer is very soft and easily scratched.
- (8) It is dangerous that moisture come into or contacted the LCD module, because moisture may damage LCD module when it is operating.
- (9) High temperature or humidity may reduce the performance of module. Please store LCD module within the specified storage conditions.
- (10) When ambient temperature is lower than 10°C may reduce the display quality, the response time will become slowly.
- (11) Do not keep same pattern in a long period of time. It may cause image sticking on LCD.

#### **11.2 SAFETY PRECAUTIONS**

- (1) Do not disassemble the module or insert anything into the Backlight unit.
- (2) If the liquid crystal material leaks from the panel, it should be kept away from the eyes or mouth. In case of contact with hands, skin or clothes, it has to be washed away thoroughly with soap.
- (3) After the module's end of life, it is not harmful in case of normal operation and storage.

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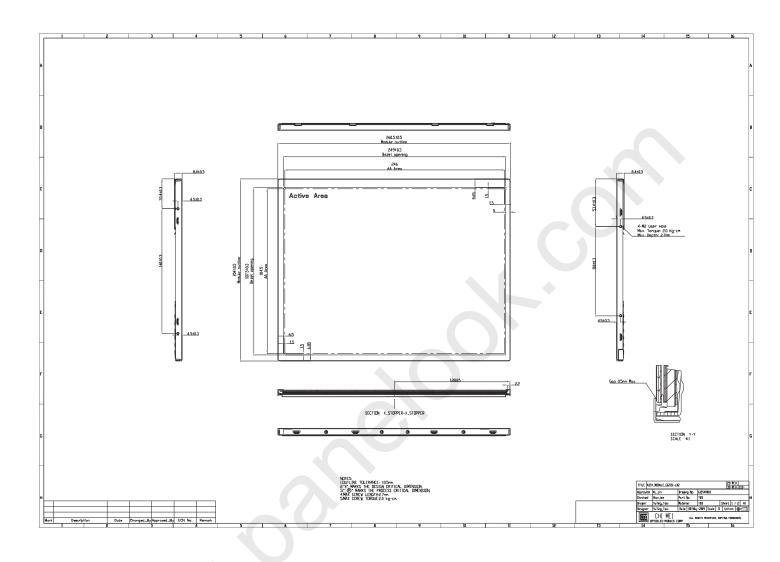
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# 12. MECHANICAL CHARACTERISTICS

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