



SHANGHAI TIANMA MICRO-ELECTRONICS

TM101JDHG33-00

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Customer : _____

Approved by	Notes

TIANMA Confirmed :

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This technical specification is subjected to change without notice

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Record of Revision

Rev	Issued Date	Description	Editor
1.0	2018-06-21	Preliminary Specification Released.	WangLuyao

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1 General Specifications

Feature		Spec
Display Spec.	Size	10.1 inch
	Resolution	1280(RGB) x 800
	Technology Type	SFT
	Pixel Configuration	R.G.B. Vertical Stripe
	Pixel Pitch (mm)	0.1695x0.1695
	Display Mode	TM with Normally Black
	Surface Treatment(Up Polarizer)	HC
	Viewing Direction	All direction
Mechanical Characteristics	LCM (W x H x D) (mm)	LCM: 229.80x149.0x6.2
	Active Area(mm)	TFT LCD:216.96x135.60
	Matching Connection Type	HD1S040HA1
	Weight (g)	TBD
Electrical Characteristics	Interface	TFT: LVDS, 6/8bit selectable
	Color Depth	262K/16.7M
	Driver IC	TFT: ST5084AA *1 ST5821AD *3

Note 1: Viewing direction for best image quality is different from TFT definition; there is a 180 degree shift.

Note 2: Requirements on Environmental Protection: Q/S0002

Note 3: LCM weight tolerance : +/- 5%

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2 Input/Output Terminals

2.1 TFT LCD Panel

Connector type: JAE HD1S040HA1 or compatible
Mating Connector: IPEX 20453-040T-1 or compatible

No	Symbol	I/O	Description	Comment
1	NC	-	No Connection	
2	VDD	P	Power Supply +3.3V	
3	VDD	P	Power Supply +3.3V	
4	VDD	P	Power Supply +3.3V	
5	NC	-	No Connection	
6	NC	-	No Connection	
7	NC	-	No Connection	
8	Rxin0-	I	-LVDS differential data input(R0~R5,G0)	
9	Rxin0+	I	+LVDS differential data input(R0~R5,G0)	
10	GND	P	Power ground	
11	Rxin1-	I	-LVDS differential data input(G1~G5,B0~B1)	
12	Rxin1+	I	+LVDS differential data input(G1~G5,B0~B1)	
13	GND	P	Power ground	
14	Rxin2-	I	-LVDS differential data input(B2~B5,HS,VS,DE)	
15	Rxin2+	I	+LVDS differential data input(B2~B5,HS,VS,DE)	
16	GND	P	Power ground	
17	RxCLK-	I	-LVDS differential data input	
18	RxCLK+	I	+LVDS differential data input	
19	GND	P	Power ground	
20	Rxin3-	I	-LVDS differential data input(R6~R7,G6~G7,B6~B7)	Connect to GND in 6 bit mode
21	Rxin3+	I	+LVDS differential data input(R6~R7,G6~G7,B6~B7)	
22	GND	P	Power ground	
23	NC	-	No Connection	
24	NC	-	No Connection	
25	GND	P	Power ground	
26	NC	-	No Connection	
27	SEL6/8	-	SEL6/8="H", 6bit; SEL6/8="L", 8bit	
28	GND	P	Power ground	
29	NC	-	No Connection	
30	NC	-	No Connection	
31	VLED_GND	P	VLED Ground	
32	VLED_GND	P	VLED Ground	
33	VLED_GND	P	VLED Ground	
34	NC	-	No Connection	
35	VLED_PWM	P	Backlight dimming control (NC: 100%duty cycle)	
36	VLED_EN	P	Backlight on/off control (1 or NC:ON , 0:OFF)	
37	NC	-	No Connection	
38	VLED	P	Backlight power supply	
39	VLED	P	Backlight power supply	
40	VLED	P	Backlight power supply	

Note: I/O definition:

I----Input P----Power/Ground

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3 Absolute Maximum Ratings

3.1 Driving TFT LCD Panel

GND=0V

Item	Symbol	MIN	MAX	Unit	Remark
Voltage Input	V _{in}	-0.50	5.00	V	Note1
Operating Temperature	T _{op}	-30.0	80.0	℃	
Storage Temperature	T _{st}	-40.0	80.0	℃	
Relative Humidity (Note2)	RH	--	≤95	%	Ta≤40℃
		--	≤85	%	40℃<Ta≤50℃
		--	≤55	%	50℃<Ta≤60℃
		--	≤36	%	60℃<Ta≤70℃
		--	≤24	%	70℃<Ta≤80℃
Absolute Humidity	AH	--	≤70	g/m ³	Ta>70℃

Table 3.1 absolute maximum rating

Note1: Input voltage include Rxin0-/+, Rxin1-/+, Rxin2-/+, Rxin3-/+, RxCLK-/+, SEL6/8, VDD.

Note2: Ta means the ambient temperature.

It is necessary to limit the relative humidity to the specified temperature range.
Condensation on the module is not allowed.



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4 Electrical Characteristics

4.1 Driving TFT LCD Panel

VCC=3.3V, GND=0V, Ta=25°C

Item	Symbol	MIN	TYP	MAX	Unit	Remark
Power supply Voltage	VDD	3.00	3.30	3.60	V	
Power supply ripple	Vp-p	-	-	100	mV	
Power supply current	IDD	-	230	-	mA	
Power consumption	P	-	756	-	mW	Note1
Differential input voltage	Vid	200	400	600	mV	
Differential input common voltage	Vcom	-	1.2	-	V	
Differential input threshold voltage	Low level	VTL	-100	-	mV	
	High level	VTH	-	100	mV	
Inrush current	Irush	-	-	TBD	A	

Table 4.1 LCD module electrical characteristics

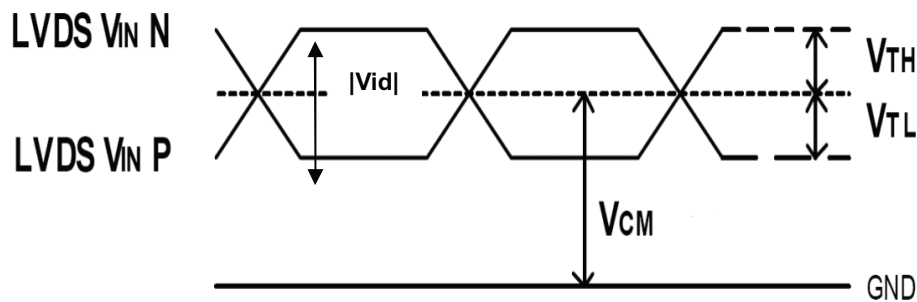


Figure 4.1 LVDS DC characteristics

Note1: To test the current dissipation, using the “color bar” testing pattern shown as below:

1. White
2. Yellow
3. Cyan
4. Green
5. Magenta
6. Red
7. Blue
8. Black

1	2	3	4	5	6	7	8
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Figure 4.1.2 Current dissipation testing pattern

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4.2 Driving Backlight

Ta=25℃

Item		Symbol	Min	Typ	Max	Unit	Remark	
Backlight power supply voltage		VLED	14	18	22	V		
Backlight power supply current		I_LED	-	330	-	mA		
Backlight power consumption		P_LED	-	5940	-	mW		
Input voltage for VLED_PWM signal	High level	-	2.0	-	5.0	V		
	Low level	-	0	-	0.4	V		
Input voltage for VLED_EN	High level	-	2.0	-	5.0	V		
	Low level	-	0	-	0.4	V		
VLED_PWM frequency		Fpwm	200	-	10k	HZ		
VLED_PWM duty		D	0.2		100	%	Note1	
Operating Life Time		--	--	40000	--	hrs	Note2	

Note 1: According to LED driver IC characteristics, the minimum value of VELD_PWM duty may vary with VLED_PWM frequency, higher the frequency, bigger the duty.

Note 2: Optical performance should be evaluated at Ta=25℃ only.

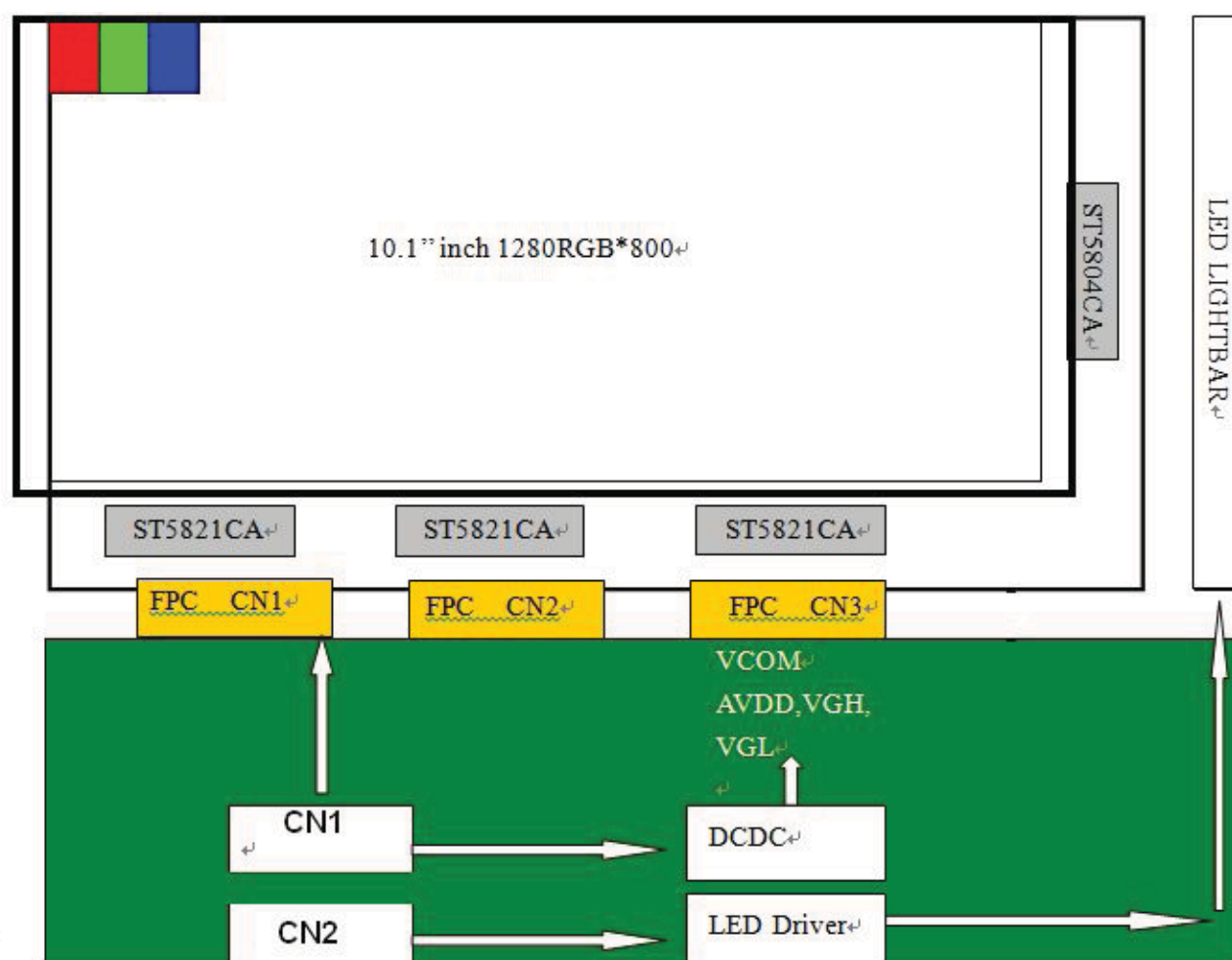
If LED is driven by high current, high ambient temperature & humidity condition. The life time of LED will be reduced.

Operating life means brightness goes down to 50% of initial brightness.

Typical operating life time is estimated data.



4.3 Block Diagram





5 Timing Chart

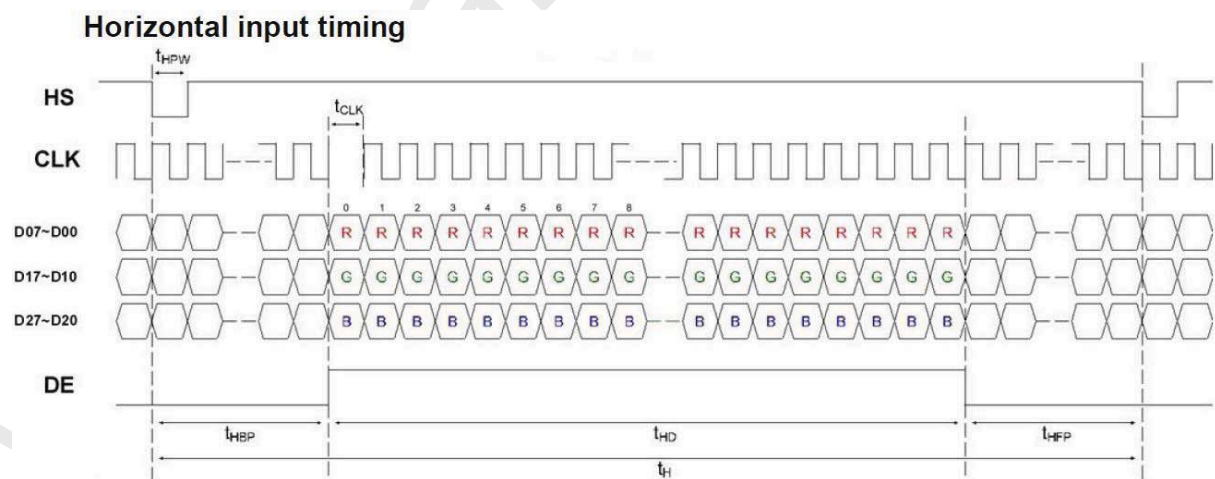
5.1 LVDS signal timing characteristics

VCC=3.3V, GND=0V, Ta=25°C

Parameter	Symbol	Min	Typ	Max	Unit	Remark
CLK frequency	1/t _{clk}	68	69	79	MHz	
Horizontal blanking time	t _{HBT}	136	140	164	t _{clk}	t _{hbp} + t _{HFP}
Horizontal back porch	t _{HBP}	5	5	164- t _{HFP}	t _{clk}	
Horizontal display area	t _{HD}	1280	1280	1280	t _{clk}	
Horizontal front porch	t _{HFP}	131	139	159	t _{clk}	
Horizontal period	t _H	1416	1424	1444	t _{clk}	
Horizontal pulse width	t _{HPW}	1	1	256	t _{clk}	
Vertical blanking time	t _{VBT}	5	42	101	t _H	t _{vbp} + t _{VFP}
Vertical back porch	t _{VBP}	2	2	101- t _{VFP}	t _H	
Vertical display area	t _{VD}	800	800	800	t _H	
Vertical front porch	t _{VFP}	3	40	99	t _H	
Vertical period	t _V	805	842	901	t _H	
Vertical pulse width	t _{VPW}	1	1	128	t _H	
Frame Rate	F	-	60	-	HZ	

Table 5.1 timing parameter

5.2 Input Clock and Data timing Diagram:



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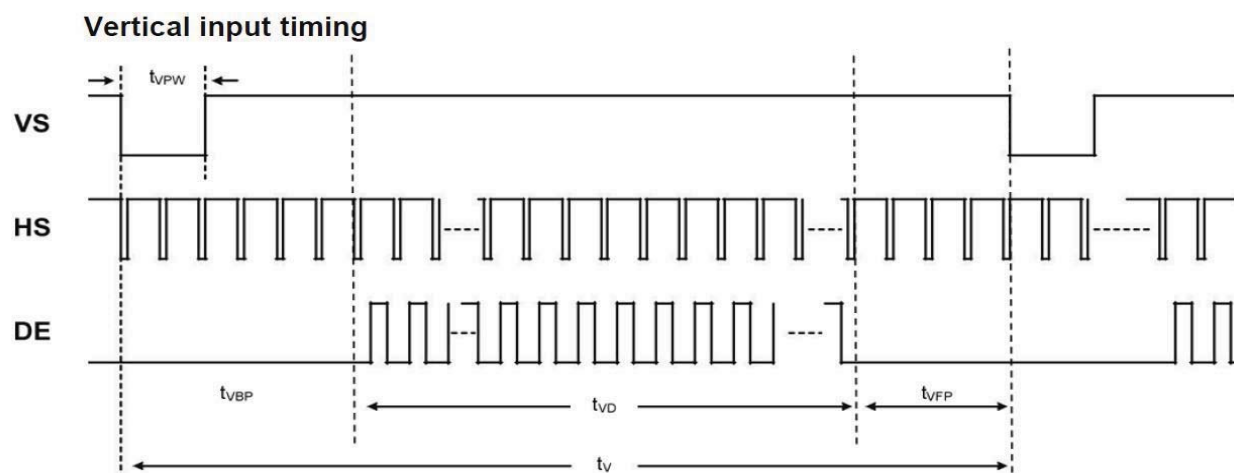
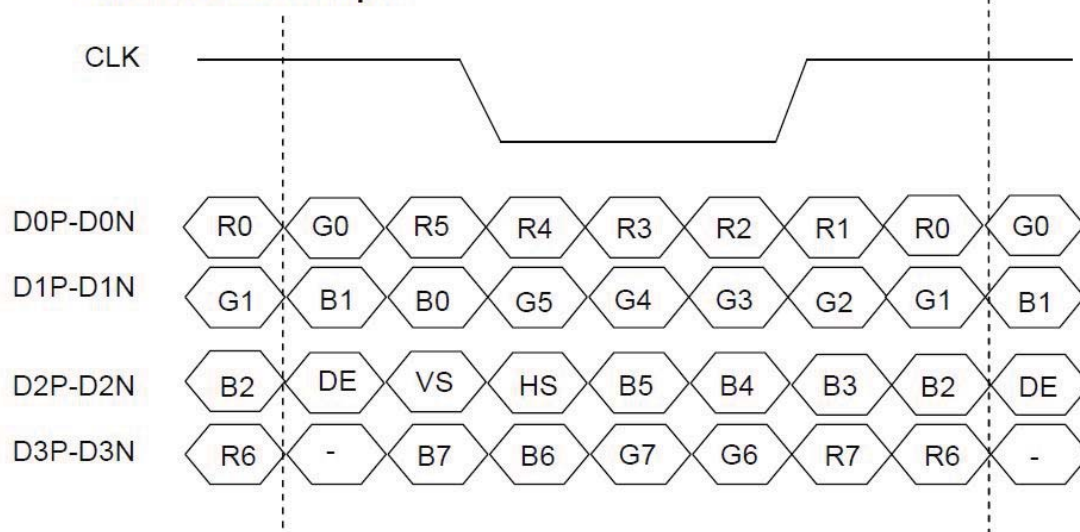


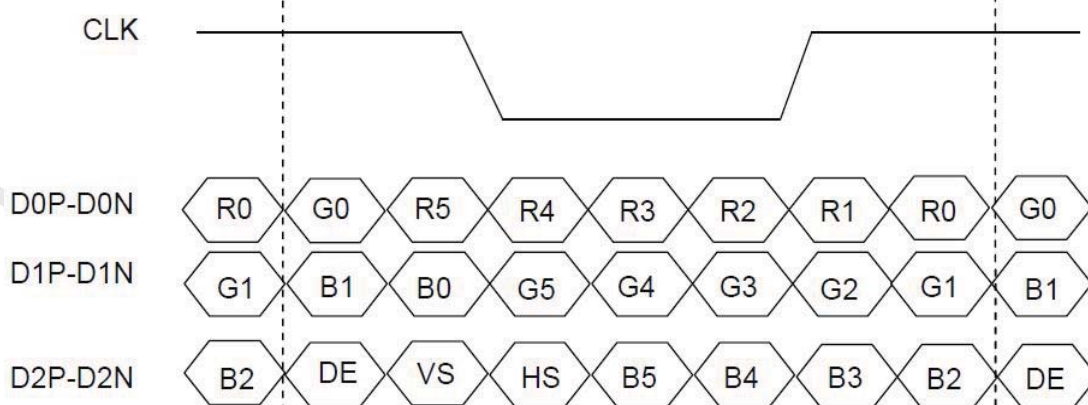
Figure 5.2 Input signal data timing

5.3 LVDS data input format

8-bit mode data input



6-bit mode data input



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5.4 Power On/Off Sequence

Item	Symbol	Min	Typ	Max	Unit	Remark
VDD on to VDD stable	Tp1	1.5	-	3	ms	
VDD stable to signal on	Tp2	4	-	50	ms	
Signal on to VLED_EN on	Tp3	200	-	-	ms	
PWM on to VLED_EN on	Tp4	0	-	200	ms	
VLED to PWM on	Tp5	10	-	-	ms	
VLED on to VELD stable	Tp6	0.5	-	10	ms	
VDD off time	Tp7	1	-	5	ms	
VDD off to next VDD on	Tp8	500	-	-	ms	
Signal off before VDD off	Tp9	110	-	-	ms	
VLED_EN off before signal off	Tp10	200	-	-	ms	
VLED_EN off before PWM off	Tp11	0	-	200	ms	
PWM off before VLED off	Tp12	10	-	-	ms	

Table 5.1 Power on/off sequence

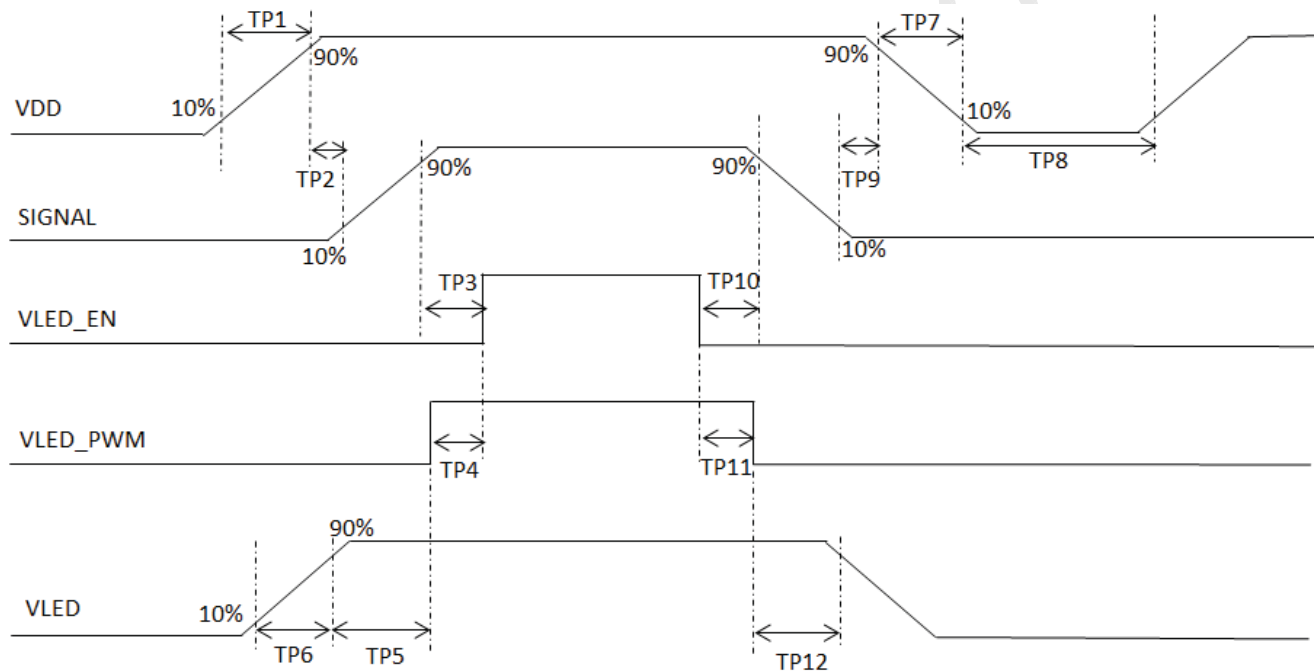


Figure 5.2 Interface power on/off sequence

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6 Optical Characteristics

Ta=25℃

Item		Symbol	Condition	Min	Typ	Max	Unit	Remark
View Angles		θT	CR≥10	75	85	-	Degree	Note 2
		θB		75	85	-		
		θL		75	85	-		
		θR		75	85	-		
Contrast Ratio		CR	θ=0°	500	700	-	-	Note1 Note3
Response Time		T _{ON} +T _{OFF}	25℃	-	25	40	ms	Note1 Note4
Chromaticity	White	x	Backlight is on	0.254	0.304	0.354	-	Note5 Note1
		y		0.280	0.330	0.380		
	Red	x		0.547	0.597	0.647		
		y		0.270	0.320	0.370		
	Green	x		0.286	0.336	0.386		
		y		0.539	0.589	0.639		
	Blue	x		0.102	0.152	0.202		
		y		0.062	0.112	0.162		
Uniformity		U	-	75	80	-	%	Note1 Note6
NTSC		-	-	45	50	-	%	Note 5
Luminance		L		750	900	-	cd/m ²	Note1 Note7

Test Conditions:

1. The ambient temperature is 25±2℃. humidity is 65±7%
2. The test systems refer to Note 1 and Note 2.

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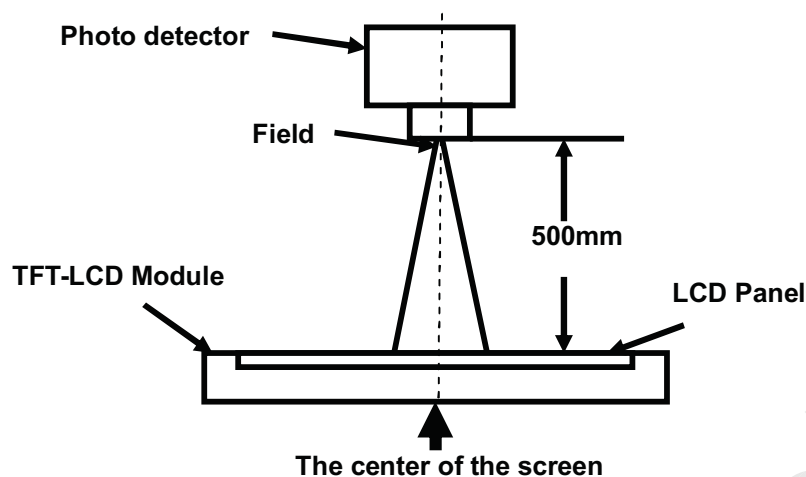


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Note 1: Definition of optical measurement system.

The optical characteristics should be measured in dark room. After 5 minutes operation, the optical properties are measured at the center point of the LCD screen. All input terminals LCD panel must be ground when measuring the center area of the panel.



Note 2: Definition of viewing angle range and measurement system.

viewing angle is measured at the center point of the LCD.

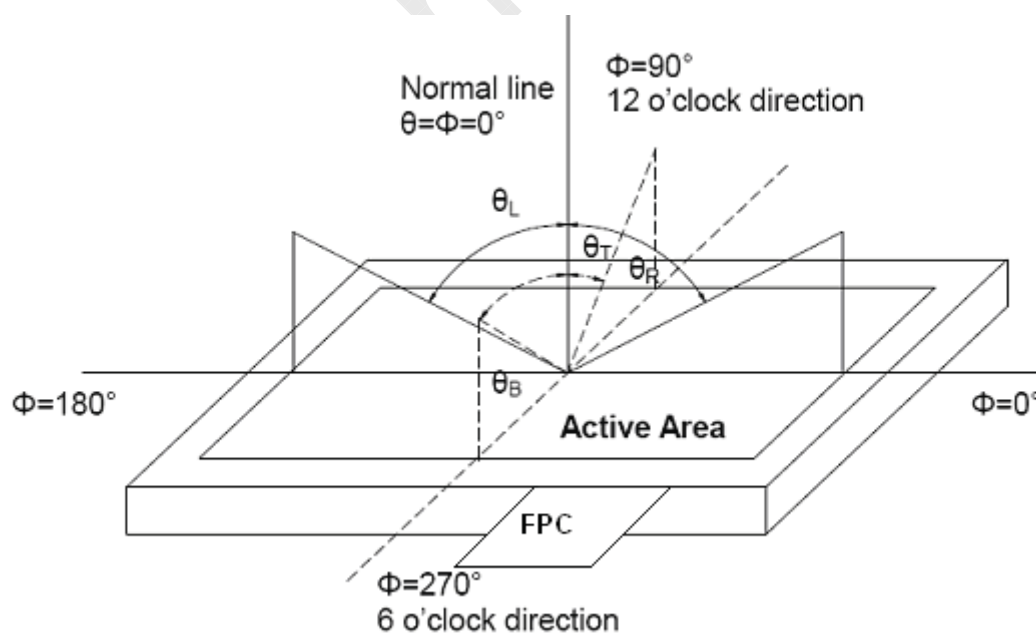


Fig. 1 Definition of viewing angle

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**Note 3: Definition of contrast ratio**

$$\text{Contrast ratio (CR)} = \frac{\text{Luminance measured when LCD is on the "White" state}}{\text{Luminance measured when LCD is on the "Black" state}}$$

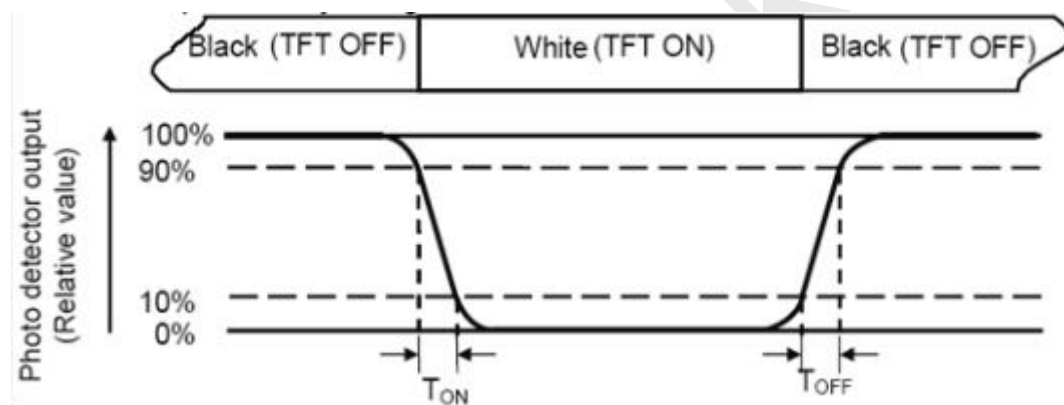
"White state ":The state is that the LCD should driven by Vwhite.

"Black state": The state is that the LCD should driven by Vblack.

Vwhite: To be determined Vblack: To be determined.

Note 4: Definition of Response time

The response time is defined as the LCD optical switching time interval between "White" state and "Black" state. Rise time (TON) is the time between photo detector output intensity changed from 90% to 10%. And fall time (TOFF) is the time between photo detector output intensity changed from 10% to 90%.

**Note 5: Definition of color chromaticity (CIE1931)**

Color coordinates measured at center point of LCD.



Note 6: Definition of Luminance Uniformity

Active area is divided into 9 measuring areas (Refer Fig. 2). Every measuring point is placed at the center of each measuring area.

$$\text{Luminance Uniformity}(U) = L_{\min} / L_{\max}$$

L-----Active area length W----- Active area width

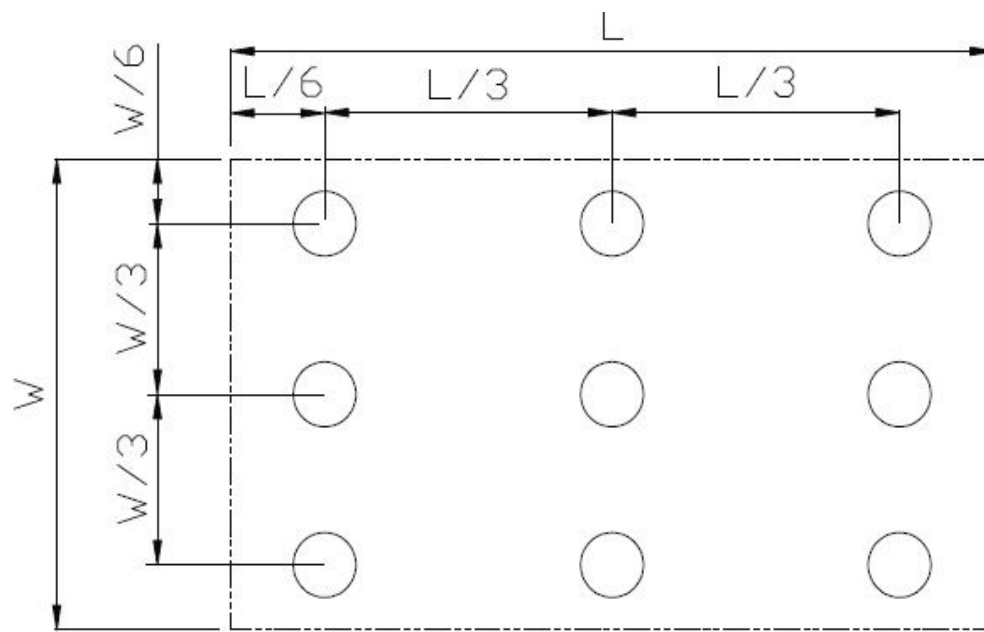


Fig. 2 Definition of uniformity

L_{\max} : The measured maximum luminance of all measurement position.

L_{\min} : The measured minimum luminance of all measurement position.

Note 7: Definition of Luminance :

Measure the luminance of white state at center point.



7 Environmental / Reliability Test

No	Test Item	Condition	Remark
1	High Temperature Operation	Ts=+80℃, 240hrs	(Note1) IEC60068-2-1:2007,GB2423.2-2008
2	Low Temperature Operation	Ta=-30℃, 240hrs	IEC60068-2-1:2007 GB2423.1-2008
3	High Temperature Storage (non-operation)	Ta=+80℃, 240hrs	IEC60068-2-1:2007 GB2423.2-2008
4	Low Temperature Storage (non-operation)	Ta=-40℃, 240hrs	IEC60068-2-1:2007 GB2423.1-2008
5	High Temperature & High Humidity Operation	Ta = +60℃, 90% RH max,240 hours	(Note2) IEC60068-2-78 :2001 GB/T2423.3—2006
6	Electro Static Discharge (operation)	C=150pF , R=330Ω , 5point/panel Air : ±8kv , 5times ; Contact : ±4kv , 5times ; (Environment : 15℃~35℃ , 30%~60% , 86Kpa~106Kpa)	IEC61000-4-2:2001 GB/T17626.2-2006
7	Package Vibration (non-operation)	Frequency range: : 5-20-200HZ , PSD : 0.01-0.01-0.001 Total:0.781g2/HZ, Times : x/y/z direction each 30min	IEC60068-2-6:1982 GB/T2423.10—1995
8	Thermal Shock (non-operation)	-30C° 30 min~+80C° 30 min, Change time:5min,100cycles	Start with cold temperature, End with high temperature, IEC60068-2-14:1984,GB2423.22-2002
9	Package Drop Test	整箱重≤10Kg , Height:80cm; 整箱重 > 10Kg, , Height:60cm; 1corner,3edges,6surfaces	IEC60068-2-32:1990 GB/T2423.8—1995

Note1: Ts is the temperature of panel's surface.

Note2: Ta is the ambient temperature of sample.

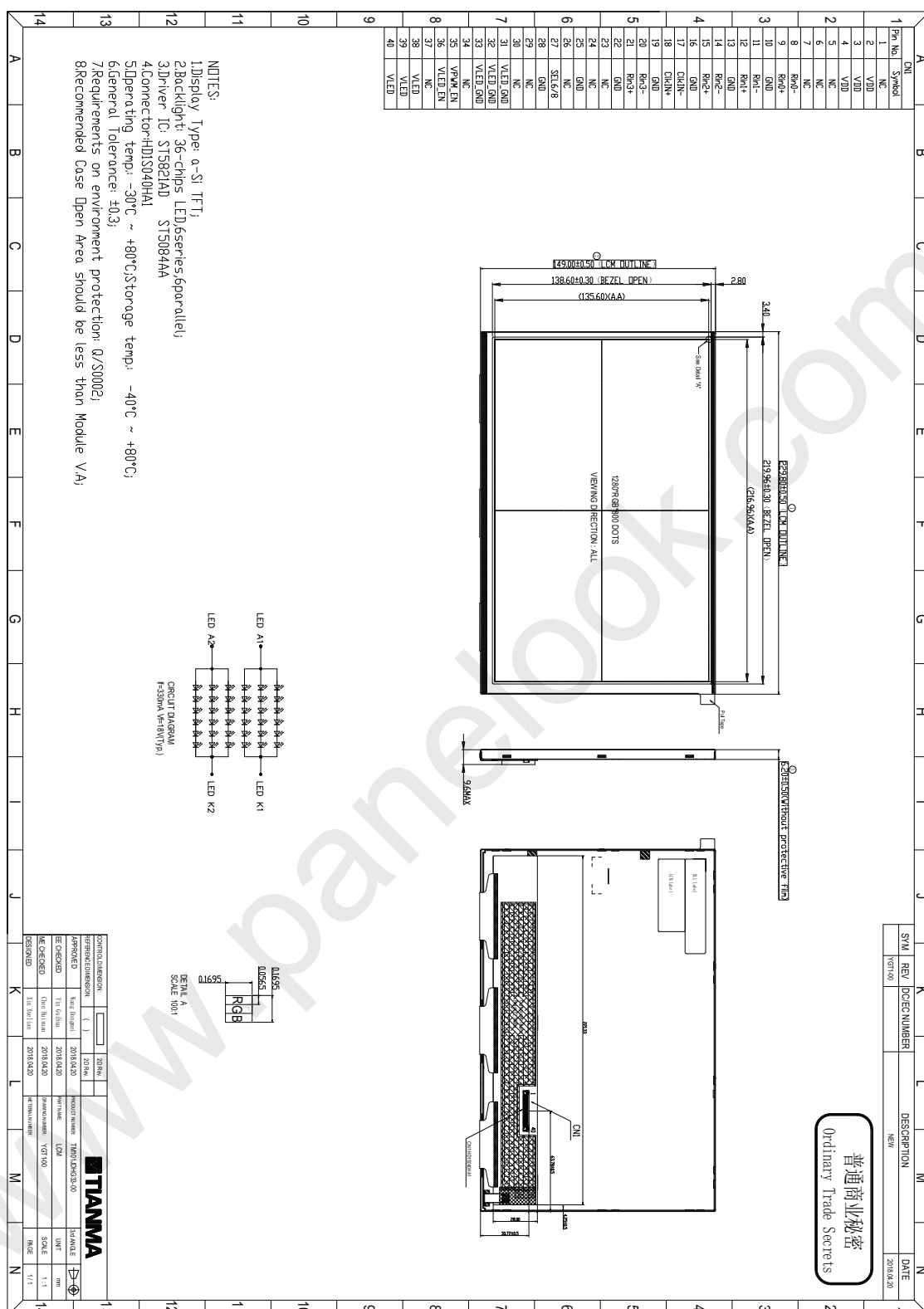
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8 Mechanical Drawing



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9 Packing Drawing

No	Item	Model (Material)	Dimensions(mm)	Unit Weight(Kg)	Quantity	Remark
1	LCM module	TM101JDHG33-00	229.8×149.0×6.2	TBD	18	
2	Tray	PET (Transmit)	485×330×22.2	TBD	12	Anti-static
3	Dust-Proof Bag	PE	700×545	0.021	1	
4	Desiccant	Desiccant	45×35	0.002	6	
5	Pearl EPE	TM101JDHG32-00 YPF1-00	293.3×228.2×1	TBD	9	
6	Pearl EPE	TM101JDHG32-00 YPF2-00	379.9×228.2×1	TBD	9	
7	BOX	CORRUGATED PAPER	520×345×74	0.3879	3	
8	Carton	CORRUGATED PAPER	544×365×250	1.01	1	
9	Label	Paper	100×52	0.0001	1	
10	Total weight	TBD				

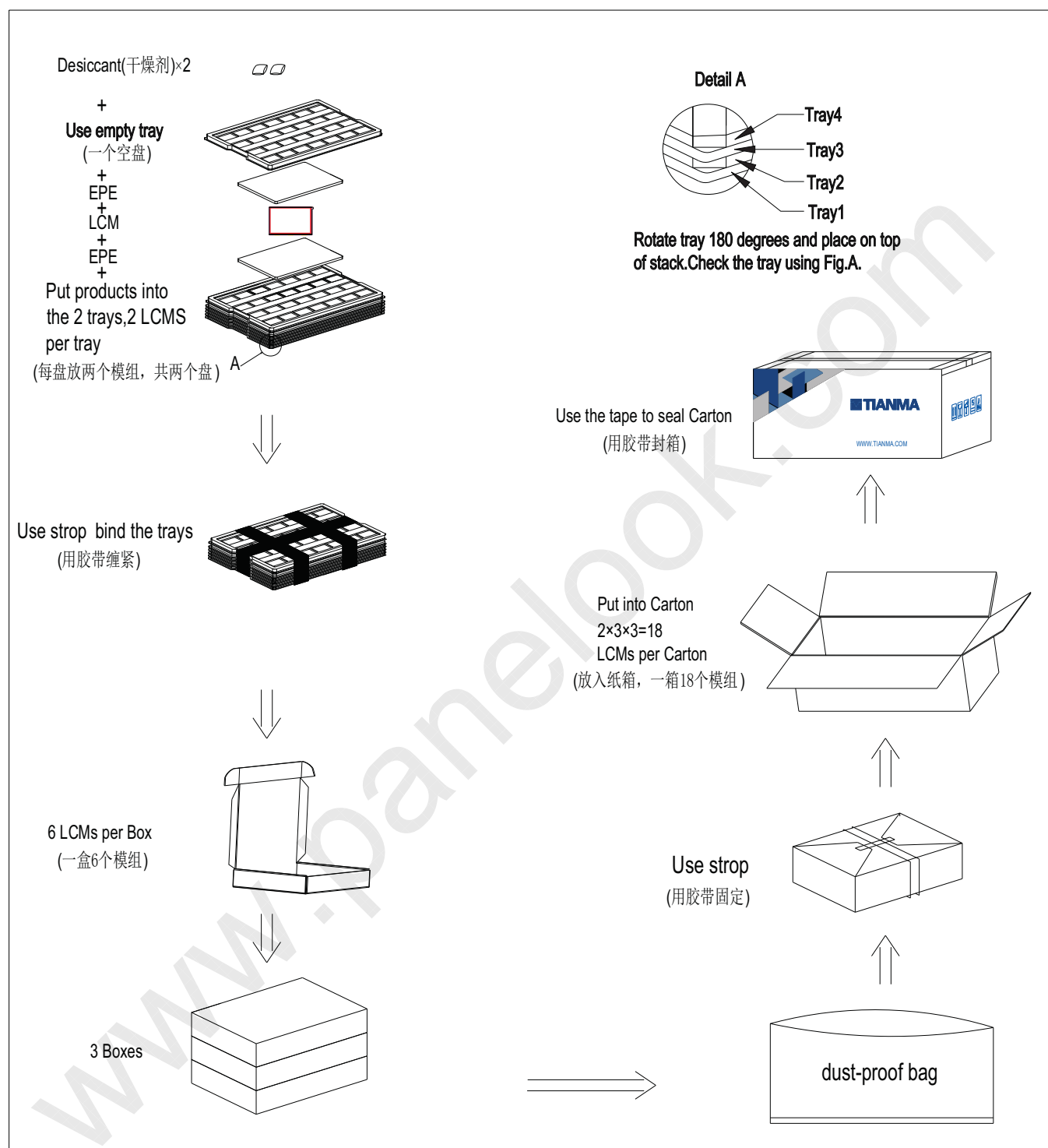
The packing method is shown as below:

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9.1 Dummy packing assembling

The packing method is shown as below:



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9.2 Stacking method(2x3x5)



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10 Precautions For Use of LCD Modules

10.1 Handling Precautions

- 10.1.1 The display panel is made of glass. Do not subject it to a mechanical shock by dropping it from a high place, etc.
- 10.1.2 If the display panel is damaged and the liquid crystal substance inside it leaks out, be sure not to get any in your mouth, if the substance comes into contact with your skin or clothes, promptly wash it off using soap and water.
- 10.1.3 Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary.
- 10.1.4 The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully.
- 10.1.5 If the display surface is contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If still not completely clear, moisten cloth with one of the following solvents:
 - Isopropyl alcohol
 - Ethyl alcoholSolvents other than those mentioned above may damage the polarizer. Especially, do not use the following:
 - Water
 - Ketone
 - Aromatic solvents
- 10.1.6 Do not attempt to disassemble the LCD Module.
- 10.1.7 If the logic circuit power is off, do not apply the input signals.
- 10.1.8 To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.
 - 10.1.8.1 Be sure to ground the body when handling the LCD Modules.
 - 10.1.8.2 Tools required for assembly, such as soldering irons, must be properly ground.
 - 10.1.8.3 To reduce the amount of static electricity generated, do not conduct assembly and other work under dry conditions.
 - 10.1.8.4 The LCD Module is coated with a film to protect the display surface. Be care when peeling off this protective film since static electricity may be generated.

10.2 Storage Precautions

- 10.2.1 When storing the LCD modules, avoid exposure to direct sunlight or to the light of fluorescent lamps.
- 10.2.2 The LCD modules should be stored under the storage temperature range. If the LCD modules will be stored for a long time, the recommend condition is:
Temperature : 0℃ ~ 40℃ Relatively humidity: ≤80%
- 10.2.3 The LCD modules should be stored in the room without acid, alkali and harmful gas.

10.3 Transportation Precautions

The LCD modules should be no falling and violent shocking during transportation, and also should avoid excessive press, water, damp and sunshine.

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