

Q/S1021-2011

	MODEL NO.	:	TM121SDS01
--	-----------	---	------------

ISSUED DATE: <u>2011/09/08</u>

VERSION : <u>1.1</u>

Preliminary Specification Final Product Specification

Customer :

Approved by	Notes

SHANGHAI AVIC Confirmed :

Prepared by	Checked by	Approved by
Hyman Chen	_	ANFERNEE DU

This technical specification is subjected to change without notice

The information contained herein is the exclusive property of SHANGHAI AVIC OPTOELECTRONICS Corporation, and shall not be distributed, reproduced, or disclosed in whole or in part without prior written permission of SHANGHAI AVIC OPTOELECTRONICS Corporation.

Page 1 of 32



Q/S1021-2011

TABLE OF CONTENTS

TABLE OF CONTENTS	2
RECORD OF REVISION	3
1. OUTLINE	4
1.1 STRUCTURE AND PRINCIPLE	4
1.2 APPLICATIONS	4
1.3 FEATURES	4
2. GENERAL SPECIFICATIONS	5
3. ABSOLUTE MAXIMUM RATINGS	
4. BLOCK DIAGRAM	7
5. MECHANICAL SPECIFICATIONS	8
6. ELECTRICAL CHARACTERISTICS	
6.1 DRIVING FOR LCD	9
6.2 DRIVING FOR BACKLIGHT	10
7. CONNECTIONS AND FUNCTIONS FOR INTERFACE PINS	11
7.1 LVDS	11
7.2 BACKLIGHT	
7.3 POSITION OF PLUGS AND A SOCKET	12
7.4 CONNECTION BETWEEN RECEIVER AND TRANSMITTER FOR LVDS	13
8. DISPLAY COLORS AND INPUT DATA SIGNALS	
9. INTERFACE TIMING	15
9.1 TIMING CHARACTERISTICS	
9.2 INPUT SIGNAL TIMING CHART	
9.3 PIXEL DATA ALIGNMENT OF DISPLAY IMAGE	
9.4. POWER SUPPLY VOLTAGE SEQUENCE	
10. OPTICS	
11. MARKINGS	
11.1 PRODUCT LABEL	
11.2 INDICATION LOCATIONS	
12. PACKING, TRANSPORTATION AND DELIVERY	
13. PRECAUTIONS	
14.OUTDRAWING	31

The information contained herein is the exclusive property of SHANGHAI AVIC OPTOELECTRONICS Corporation, and shall not be distributed, reproduced, or disclosed in whole or in part without prior written permission of SHANGHAI AVIC OPTOELECTRONICS Corporation.

Page 2 of 32



Q/S1021-2011

RECORD OF REVISION

Rev	Issued Date	Description	Editor
1.0	2011-6-16	Preliminary Release	Wei Zhang
1.1	2011-8-22	Change the INTERFACE PINS	Hyman Chen
			,

The information contained herein is the exclusive property of SHANGHAI AVIC OPTOELECTRONICS Corporation, and shall not be distributed, reproduced, or disclosed in whole or in part without prior written permission of SHANGHAI AVIC OPTOELECTRONICS Corporation.

Page 3 of 32

Q/S1021-2011



1. OUTLINE

1.1 STRUCTURE AND PRINCIPLE

SHANGHAI AVIC OPTOELECTRONICS

TM121SDS01 module is composed of the amorphous silicon thin film transistor liquid crystal display (a-Si TFT LCD) panel structure with driver LSIs for driving the TFT (Thin Film Transistor) array and a acklight. The a-Si TFT LCD panel structure is injected liquid crystal material into a narrow gap between the TFT array glass substrate and a color-filter glass substrate.

Color (Red, Green, Blue) data signals from a host system (e.g. PC, signal generator, etc.) are modulated into best form for active matrix system by a signal processing board, and sent to the driver LSIs which drive the individual TFT arrays. The TFT array as an electro-optical switch regulates the amount of transmitted light from the backlight assembly, when it is controlled by data signals. Color images are created by regulating the amount of transmitted light through the TFT array of red, green and blue dots.

1.2 APPLICATIONS

· Monitor for industrial display

1.3 FEATURES

- a-Si TFT active matrix
- LVDS interface
- R.G.B input 8bit, 16.2 millions colors (6bit+FRC)
- Resolution SVGA (800× 600 pixels)
- Wide viewing angle 80°/80° (L/R); 65°/75° (U/D)
- Contrast ratio 700 :1
- Module size 279.0 (H) ×209.0 (V) ×9.0 (D) mm
- Fast response time (Ton+ Toff= 35 ms)
- Color gamut (55%)
- Edge light type backlight (White-LED)
- Inverter less
- RoHS compliance
- TCO 5.0 compliance

The information contained herein is the exclusive property of SHANGHAI AVIC OPTOELECTRONICS Corporation, and shall not be distributed, reproduced, or disclosed in whole or in part without prior written permission of SHANGHAI AVIC OPTOELECTRONICS Corporation.

Page 4 of 32



Q/S1021-2011

2. GENERAL SPECIFICATIONS

Display area246.0 (H) × 184.5 (V) mm (typ.)Diagonal size of display30.7 cm (12.1 inches)Drive systema-Si TFT active matrixDisplay color16.2 M colors (6bit+FRC)Pixel800 (H) × 600(V) pixelsPixel arrangementRGB vertical stripePixel pitch0.3075 (H) × 0.3075 (V) mmModule size279.0 (H) × 209.0 (V) ×9.0 (D) mmWeightTBDContrast ratio700 :1 (typ.)Viewing angle160°/ 135° (typ.)Color gamut55 % (typ.)Response time35 ms (typ.)Luminance450 cd/m² (typ.)Transmissive ModeNormally WhiteSurface TreatmentAnti GlareSignal systemLCD panel signal processing board: 3.3VBacklightWhite-LEDPower consumptionTBD		
Drive systema-Si TFT active matrixDisplay color16.2 M colors (6bit+FRC)Pixel800 (H) × 600(V) pixelsPixel arrangementRGB vertical stripePixel pitch0.3075 (H) × 0.3075 (V) mmModule size279.0 (H) ×209.0 (V) ×9.0 (D) mmWeightTBDContrast ratio700 :1 (typ.)Viewing angle160°/ 135° (typ.)Color gamut55 % (typ.)Response time35 ms (typ.)Luminance450 cd/m² (typ.)Transmissive ModeNormally WhiteSurface TreatmentAnti GlareSignal systemLVDS 1portPower supply voltageLCD panel signal processing board: 3.3VBacklightWhite-LED	Display area	246.0 (H) × 184.5 (V) mm (typ.)
Display color16.2 M colors (6bit+FRC)Pixel800 (H) × 600(V) pixelsPixel arrangementRGB vertical stripePixel pitch0.3075 (H) × 0.3075 (V) mmModule size279.0 (H) ×209.0 (V) ×9.0 (D) mmWeightTBDContrast ratio700 :1 (typ.)Viewing angle160°/ 135° (typ.)Color gamut55 % (typ.)Response time35 ms (typ.)Luminance450 cd/m² (typ.)Transmissive ModeNormally WhiteSurface TreatmentAnti GlareSignal systemLVDS 1 portPower supply voltageLCD panel signal processing board: 3.3VBacklightWhite-LED	Diagonal size of display	30.7 cm (12.1 inches)
Pixel800 (H) × 600(V) pixelsPixel arrangementRGB vertical stripePixel pitch0.3075 (H) × 0.3075 (V) mmModule size279.0 (H) ×209.0 (V) ×9.0 (D) mmWeightTBDContrast ratio700 :1 (typ.)Viewing angle160°/ 135° (typ.)Color gamut55 % (typ.)Response time35 ms (typ.)Luminance450 cd/m² (typ.)Transmissive ModeNormally WhiteSurface TreatmentAnti GlareSignal systemLVDS 1 portPower supply voltageLCD panel signal processing board: 3.3VWhite-LEDWhite-LED	Drive system	a-Si TFT active matrix
Pixel arrangementRGB vertical stripePixel pitch0.3075 (H) × 0.3075 (V) mmModule size279.0 (H) ×209.0 (V) ×9.0 (D) mmWeightTBDContrast ratio700 :1 (typ.)Viewing angle160°/ 135° (typ.)Color gamut55 % (typ.)Response time35 ms (typ.)Luminance450 cd/m² (typ.)Transmissive ModeNormally WhiteSurface TreatmentAnti GlareSignal systemLVDS 1portPower supply voltageLCD panel signal processing board: 3.3VBacklightWhite-LED	Display color	16.2 M colors (6bit+FRC)
Pixel pitch0.3075 (H) × 0.3075 (V) mmModule size279.0 (H) ×209.0 (V) ×9.0 (D) mmWeightTBDContrast ratio700 :1 (typ.)Viewing angle160°/ 135° (typ.)Color gamut55 % (typ.)Response time35 ms (typ.)Luminance450 cd/m² (typ.)Transmissive ModeNormally WhiteSurface TreatmentAnti GlareSignal systemLVDS 1portPower supply voltageLCD panel signal processing board: 3.3VBacklightWhite-LED	Pixel	800 (H) × 600(V) pixels
Module size279.0 (H) ×209.0 (V) ×9.0 (D) mmWeightTBDContrast ratio700 :1 (typ.)Viewing angle160°/ 135° (typ.)Color gamut55 % (typ.)Response time35 ms (typ.)Luminance450 cd/m² (typ.)Transmissive ModeNormally WhiteSurface TreatmentAnti GlareSignal systemLVDS 1 portPower supply voltageLCD panel signal processing board: 3.3VBacklightWhite-LED	Pixel arrangement	RGB vertical stripe
WeightTBDContrast ratio700 :1 (typ.)Viewing angle160°/ 135° (typ.)Color gamut55 % (typ.)Response time35 ms (typ.)Luminance450 cd/m² (typ.)Transmissive ModeNormally WhiteSurface TreatmentAnti GlareSignal systemLVDS 1portPower supply voltageLCD panel signal processing board: 3.3VBacklightWhite-LED	Pixel pitch	0.3075 (H) × 0.3075 (V) mm
Contrast ratio700 :1 (typ.)Viewing angle160°/ 135° (typ.)Color gamut55 % (typ.)Response time35 ms (typ.)Luminance450 cd/m² (typ.)Transmissive ModeNormally WhiteSurface TreatmentAnti GlareSignal systemLVDS 1portPower supply voltageLCD panel signal processing board: 3.3VBacklightWhite-LED	Module size	279.0 (H) ×209.0 (V) ×9.0 (D) mm
Viewing angle160° / 135° (typ.)Color gamut55 % (typ.)Response time35 ms (typ.)Luminance450 cd/m² (typ.)Transmissive ModeNormally WhiteSurface TreatmentAnti GlareSignal systemL/DS 1portPower supply voltageLCD panel signal processing board: 3.3VBacklightWhite-LED	Weight	TBD
Color gamut55 % (typ.)Response time35 ms (typ.)Luminance450 cd/m² (typ.)Transmissive ModeNormally WhiteSurface TreatmentAnti GlareSignal systemLVDS 1portPower supply voltageLCD panel signal processing board: 3.3VBacklightWhite-LED	Contrast ratio	700 :1 (typ.)
Response time35 ms (typ.)Luminance450 cd/m² (typ.)Transmissive ModeNormally WhiteSurface TreatmentAnti GlareSignal systemLVDS 1portPower supply voltageLCD panel signal processing board: 3.3VBacklightWhite-LED	Viewing angle	160°/ 135° (typ.)
Luminance450 cd/m² (typ.)Transmissive ModeNormally WhiteSurface TreatmentAnti GlareSignal systemLVDS 1portPower supply voltageLCD panel signal processing board: 3.3VBacklightWhite-LED	Color gamut	55 % (typ.)
Transmissive ModeNormally WhiteSurface TreatmentAnti GlareSignal systemLVDS 1portPower supply voltageLCD panel signal processing board: 3.3VBacklightWhite-LED	Response time	35 ms (typ.)
Surface TreatmentAnti GlareSignal systemLVDS 1portPower supply voltageLCD panel signal processing board: 3.3VBacklightWhite-LED	Luminance	450 cd/m ² (typ.)
Signal system LVDS 1port Power supply voltage LCD panel signal processing board: 3.3V Backlight White-LED	Transmissive Mode	Normally White
Power supply voltage LCD panel signal processing board: 3.3V Backlight White-LED	Surface Treatment	Anti Glare
Backlight White-LED	Signal system	LVDS 1port
	Power supply voltage	LCD panel signal processing board: 3.3V
Power consumption TBD	Backlight	White-LED
	Power consumption	TBD

The information contained herein is the exclusive property of SHANGHAI AVIC OPTOELECTRONICS Corporation, and shall not be distributed, reproduced, or disclosed in whole or in part without prior written permission of SHANGHAI AVIC OPTOELECTRONICS Corporation.

Page 5 of 32



Q/S1021-2011

3. ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Rating	Unit	Remarks
Power Supply Voltage	-0.3 ~ +6.0	V	Ta = 25°C	Ta = 25°C
Input voltage for signals	Vi	-0.3 ~ +3.3	V	Ta = 25°C
Light bar peak forward current	I _F	≤ 150	mArms	Note 3
Storage temperature	Tst	-30 ~ +80	°C	Note 4
Operating temperature	Тор	-20 ~ +70	°C	Note 4, 5
Absolute humidity	AH	≤ 70	g/m ³	Ta > 50°C
Operating altitude	-	≤ 4,850	m	-20°C≤ Ta≤ 70°C
Storage altitude	-	≤ 13,600	m	-30°C≤ Ta≤ 80°C

Note1: Display signals are DA0+/-, DA1+/-, DA2+/-, DA3+/-, CKA+/-, DB0+/-, DB1+/-, DB2+/-, DB3+/-,

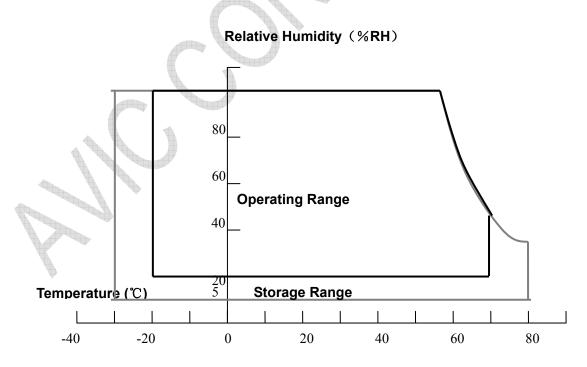
and CKB+/-.

Note2: Function signal is MSL.

Note3: Temperature and relative humidity range is shown in the figure below.

- (a) 90%RH Max. (Ta≤ 40°C)
- (b) Wet-bulb temperature should be39°C Max. (Ta> 40°C)
- (c) No condensation.

Note4: The temperature of panel display surface area should be -20°C Min and 80°C Max.



The information contained herein is the exclusive property of SHANGHAI AVIC OPTOELECTRONICS Corporation, and shall not be distributed, reproduced, or disclosed in whole or in part without prior written permission of SHANGHAI AVIC OPTOELECTRONICS Corporation.

Page 6 of 32



SHANGHAI AVIC OPTOELECTRONICS Q/S1021-2011 4. BLOCK DIAGRAM I/F LCD MODULE DA0+ 100Ω ⋛ DA0-DA1+ 100Ω ≶ DA1-DA2+ 100Ω ≶ DA2-CKA+ 100Ω ≹ CKA-Timing Controller LVDS Receiver DA3+ 100Ω ≶ DA3-DB0+ 100Ω ≹ DB0-DB1+ 100Ω ≹ Source Driver DB1-DB2+ 100Ω ≶ DB2-CKB+ 100Ω ≹ LCD Panel CKA-Gate Driver $H:800 \times 3 (R,G,B)$ DB3+ 100Ω ≹ DB3-V: 600 Fuse DC/DC VDD Power Edge side backlight Converter 12V, On/Off LED Driver Dimming FG GND

Note: System ground (GND), FG (Frame ground) in the product should be connected together in customer equipment.

The information contained herein is the exclusive property of SHANGHAI AVIC OPTOELECTRONICS Corporation, and shall not be distributed, reproduced, or disclosed in whole or in part without prior written permission of SHANGHAI AVIC OPTOELECTRONICS Corporation.

Page 7 of 32



Q/S1021-2011

5. MECHANICAL SPECIFICATIONS

Parameter	Specification	Unit
Module size	279.0± 0.5 (W) × 209.0 ± 0.5 (H) × 9.0(D)	mm
Display area	246.0(H) × 184.5(V) mm (typ.), [30.75 cm (12.1 inches)]	mm
Weight	TBD	g

The information contained herein is the exclusive property of SHANGHAI AVIC OPTOELECTRONICS Corporation, and shall not be distributed, reproduced, or disclosed in whole or in part without prior written permission of SHANGHAI AVIC OPTOELECTRONICS Corporation.

Page 8 of 32



Q/S1021-2011

6. ELECTRICAL CHARACTERISTICS

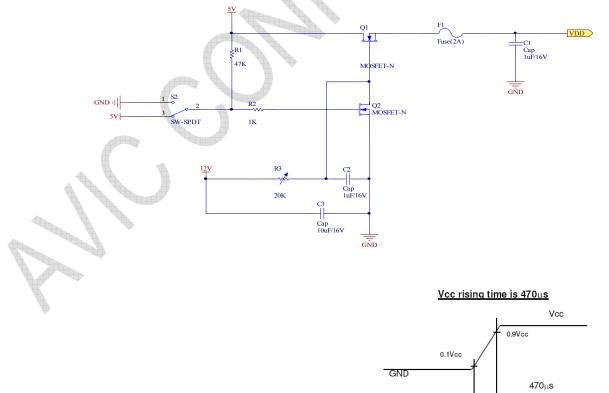
6.1 DRIVING FOR LCD

Parameter		Symbol	min.	typ.	max.	Unit	Remarks
Power supply voltage		VDD	3.0	3.3	3.6	V	-
Power supply current		IDD	-	-	325 Note 1	mA	at VDD = 3.3V
Permissible ripple voltage		VRP	-	-	100	mV	VDD
Differential input voltage		Vid	250		450	mV	
Differential input threshold	Low	VTL	-100	-	-	mV	VCM = 1.25V
voltage for LVDS receiver	High	VTH	-	-	100	mV	Note2
Input voltage width for receiver	LVDS	Vi	0	-	2.4	V	-
Terminating resistor		RT	-	100	-	Ω	-
Rush current		I _{rush}	-		1.5	А	Note3

Note 1: All black pattern

Note 2: Common mode voltage for LVDS receiver

Note 3: Measurement Conditions:



The information contained herein is the exclusive property of SHANGHAI AVIC OPTOELECTRONICS Corporation, and shall not be distributed, reproduced, or disclosed in whole or in part without prior written permission of SHANGHAI AVIC OPTOELECTRONICS Corporation.

Page 9 of 32



SHANGHAI AVIC OPTOELECTRONICS 6.2 DRIVING FOR BACKLIGHT

Q/S1021-2011

						(Ta=25°C) Note1
Parameter	Symbol	min.	typ.	max.	Unit	Remarks
Light bar operation voltage (for reference)	V _{LED}	25	-	33	Vrms	Operating with fixed driving current
Light bar operation current (pin)	I _{LED}	-	80	-	mArms	Note1
Light bar operating lifetime	Hr	50000	-	-	Hour	I _{LED} =80mA,Note3

Note1: The backlight of this product is made up of 1 light bar, LED to be 3020, 20pieces, 10 serials and 2 parallels.

The information contained herein is the exclusive property of SHANGHAI AVIC OPTOELECTRONICS Corporation, and shall not be distributed, reproduced, or disclosed in whole or in part without prior written permission of SHANGHAI AVIC OPTOELECTRONICS Corporation.

Page 10 of 32

Note2: The light bar can work normally if the PWM dimming ratio range is from 0% to 100% and the operation current is 80mA.

Note3: The operating lifetime is mean time to half-luminance. In case the product works under room temperature environment.



Q/S1021-2011

7. CONNECTIONS AND FUNCTIONS FOR INTERFACE PINS

7.1 LVDS

CN1: MSB240420HE (Produced by STM) or equivalent.

Pin	Name	Description
1	VCC	3.3V Power Supply
2	VCC	3.3V Power Supply
3	GND	Ground
4	6-8Bit SEL	Select 6 or 8 Bits LVDS Input (VCC:8Bits ; GND/NC: 6Bits)
5	RIN0-	Negative(-) LVDS differential data input
6	RIN0+	Positive(+) LVDS differential data input
7	GND	Ground
8	RIN1-	Negative(-) LVDS differential data input
9	RIN1+	Positive(+) LVDS differential data input
10	GND	Ground
11	RIN2-	Negative(-) LVDS differential data input
12	RIN2+	Positive(+) LVDS differential data input
13	GND	Ground
14	CLKIN-	Clock Signal(-)
15	CLKIN+	Clock Signal(+)
16	GND	Ground
17	RIN3-	Negative(-) LVDS differential data input
17	RIN3-	(Used for 8Bits LVDS Input; NC for 6Bits)
18	RIN3+	Positive(+) LVDS differential data input
10	TAINST	(Used for 8Bits LVDS Input; NC for 6Bits)
19	REVERSE	Display Reversed Function
13		(VCC: Display Reverse; GND/NC: Normal Display)
20	NC/GND	Test Function Pin(Do not set this pin to High)

The information contained herein is the exclusive property of SHANGHAI AVIC OPTOELECTRONICS Corporation, and shall not be distributed, reproduced, or disclosed in whole or in part without prior written permission of SHANGHAI AVIC OPTOELECTRONICS Corporation.

Page 11 of 32



Q/S1021-2011

SHANGHAI AVIC OPTOELECTRONICS

7.2 BACKLIGHT

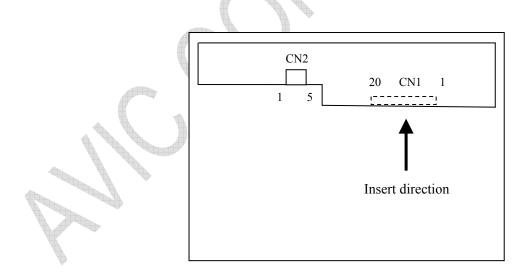
CN2: MSB24038P5 (Produced by STM) or equivalent.

Pin	Symbol	Signal Name	
1	Vcc	12V	
2	GND	GND	
3	Enable	5V-On / 0V-Off	
4	Dimming	PWM Dimming	
5	NC	NC	

PWM Dimming:

Parame	eter	Symbol	min.	typ.	max.	Unit
PWM Input	Logic-High	V _{PWMH}	1.2		-	V
Threshold Voltage	Logic-Low				0.4	V
PWM Input Fi	requency	1/T _{PWM}	100	200	10K	Hz

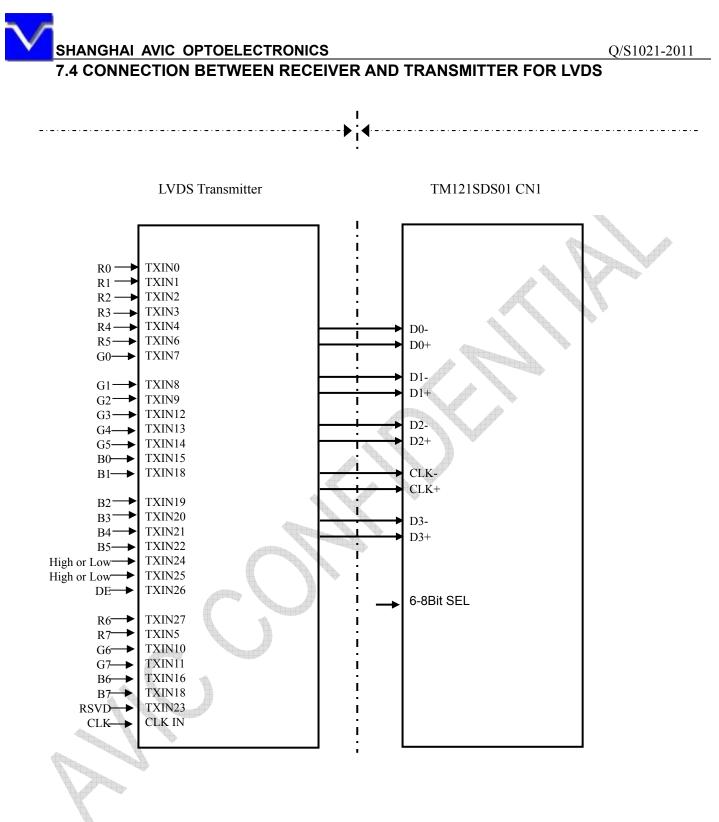
7.3 POSITION OF PLUGS AND A SOCKET



The information contained herein is the exclusive property of SHANGHAI AVIC OPTOELECTRONICS Corporation, and shall not be distributed, reproduced, or disclosed in whole or in part without prior written permission of SHANGHAI AVIC OPTOELECTRONICS Corporation.

Page 12 of 32





Note1: The lowest bit (RA0, GA0, BA0, RB0, GB0, BB0), the most upper bit (RA7, GA7, BA7, RB7, GB7, BB7)

Note2:Connecting cable between LCD panel's connector and transmitter should use 100Ω twisted line. Note3: If only Hsync and Vsync, the product don't work. Make sure DE signal has been input.

The information contained herein is the exclusive property of SHANGHAI AVIC OPTOELECTRONICS Corporation, and shall not be distributed, reproduced, or disclosed in whole or in part without prior written permission of SHANGHAI AVIC OPTOELECTRONICS Corporation.

Page 13 of 32



Q/S1021-2011

8. DISPLAY COLORS AND INPUT DATA SIGNALS

This product can display in equivalent to 16.2M colors in 256 scales. Also the relation between display colors and input data signals is as the following table.

Diard							D)ata	a sig	nal	()	0:Lo	ow l	leve	el,	1:H	igh	Lev	(el)						
Dispi	ay colors	R7	R6	R5	R4	R3	R2	R1	R0	G7	G6	G5	G4	G3	G2	G1	G0	B7	B6	B5	B4	В3	B2	B1	В0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
ъ	Red	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Col	Magenta	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
Basic Color	Green	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
Ba	Cyan	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
		0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
cale	Dark	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Red grayscale					:									:	<i>.</i>							:			
d gr					:			4					Ŵ												
Re	Bright	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
		1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ale	Darda	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0
/sce	Dark ♠	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0
Green grayscale					:																				
en	Bright				:	•	•	•	•					:		•			•	•	•	:	•	•	
Gre		0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0
	Green	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0
	Black	0	0	0	0	0	0	0	0	1 0	1	0	0	0	1	1	1	0	0	0	0	0	0	0	0
	DIACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
ale	Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
/sce		0	0	0		0	0	0	0	0	0	0	0		0	0	0	0	0	0	U		0	I	0
gra)					•																				
Blue grayscale	▼ Bright	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1
В		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
<u> </u>	l									1								1							

The information contained herein is the exclusive property of SHANGHAI AVIC OPTOELECTRONICS Corporation, and shall not be distributed, reproduced, or disclosed in whole or in part without prior written permission of SHANGHAI AVIC OPTOELECTRONICS Corporation.

Page 14 of 32



Q/S1021-2011

9. INTERFACE TIMING

9.1 TIMING CHARACTERISTICS

Р	arameter	Symbol	min.	typ.	max.	Unit	Remarks	ð
		1/tc	33.16 39.80 49.74 N		MHz	LVDS	V	
	Frequency	tc	30.16	25.13	20.10	ns	transmitter input	
Clock	Rise time, Fall time	-		er to the til teristics o	dittere	ns		
	Duty	-	1	transmitte	r i	-	Note 1	
	Civele	th	14.8	18.0	26.5	μs		
Horizonta I signals	Cycle	ui	920	1056	1240	CLK	55.5kHz(typ.)	
i signais	Display period	thd		800		CLK	-	
Vertical	Cycle	tv	13.3	16.67	20	ms	60.0Hz(typ.)	
signals	Cycle	lv (608	628	650	Н	00.0H2(typ.)	
Signals	Display period	tvd		600		Н	-	
	Setup time		Dofe	v r to tho ti	mina	ns		
DE/Data	Hold time	-		er to the til teristics o	•	ns	Note 1	
DE/Dala	Rise time, Fall time			transmitte		ns		

Note1: See the data sheet of LVDS transmitter.

The information contained herein is the exclusive property of SHANGHAI AVIC OPTOELECTRONICS Corporation, and shall not be distributed, reproduced, or disclosed in whole or in part without prior written permission of SHANGHAI AVIC OPTOELECTRONICS Corporation.

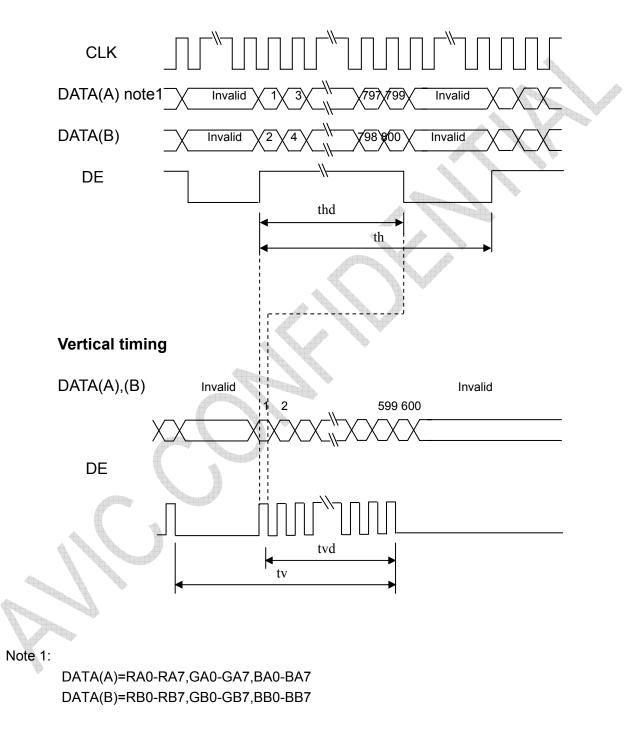
Page 15 of 32



Q/S1021-2011

9.2 INPUT SIGNAL TIMING CHART

Horizontal timing



The information contained herein is the exclusive property of SHANGHAI AVIC OPTOELECTRONICS Corporation, and shall not be distributed, reproduced, or disclosed in whole or in part without prior written permission of SHANGHAI AVIC OPTOELECTRONICS Corporation.

Page 16 of 32

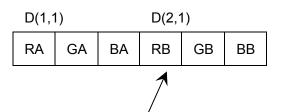


Q/S1021-2011

9.3 PIXEL DATA ALIGNMENT OF DISPLAY IMAGE

The following chart is the coordinates of per pixel

Odd Pixel: RA= R DATA GA= G DATA BA= B DATA Even Pixel : RB=R DATA GB=G DATA BB=B DATA



D(1,1)	D(2,1)	D(3,1)		D(800,1)
D(1,2)	D(2,2)	D(3,2)	· · · · · ·	D(800,2)
D(1,3)	D(2,3)	D(3,3)	•••	D(800,3)
•	•		•••	•
•	•		•••	•
•	•		•••	•
D(1,600)	D(2,600)	D(2,600)	•••	D(800,600)

The information contained herein is the exclusive property of SHANGHAI AVIC OPTOELECTRONICS Corporation, and shall not be distributed, reproduced, or disclosed in whole or in part without prior written permission of SHANGHAI AVIC OPTOELECTRONICS Corporation.

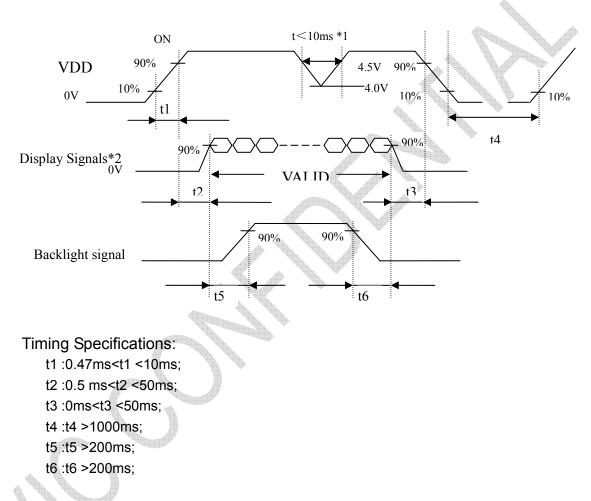
Page 17 of 32



Q/S1021-2011

9.4. POWER SUPPLY VOLTAGE SEQUENCE

9.4.1 The sequence of backlight and power



*1. When VDD is on, but the value is lower than 4.5V, a protection circuit may work, then the module may not display.

*2 The signal line is not connected with the module, at the end of cable the terminal resistor of 100Ω should be added.

Note1: Display signals (D0+/-, D1+/-, D2+/-, D3+/- and CK+/-) must be "0" voltage, exclude the VALID period (See above sequence diagram). If these signals are higher than 0.3 V, the internal circuit is damaged.

If some of display signals of this product are cut while this product is working, even if the signal input to it once again, it might not work normally. If customer stops the display signals, they should cut VDD.

Note2: When VDD is on, it should be set above 4.0V.

The information contained herein is the exclusive property of SHANGHAI AVIC OPTOELECTRONICS Corporation, and shall not be distributed, reproduced, or disclosed in whole or in part without prior written permission of SHANGHAI AVIC OPTOELECTRONICS Corporation.

Page 18 of 32



Q/S1021-2011

Note3: The backlight power supply voltage should be inputted within the valid period of display and function signals, in order to avoid unstable data display.

9.4.2 Power supply voltage ripple

When the power supply is designed, the next form can give the reference. If the voltage ripple is over the value in next form, the noise should be seen in display area.

Ripple (Measured at input terminal of power supply)

	VDD (3.3V to drive the panel)	. •
Ripple voltage	≤200mVP-P(Including spike noise)	

9.4.3 Fuse

Parameter	Fu	se	Rating		Domarka
	Туре	Supplier	Rating	Fusing current	Remarks
VDD	FCC16152ABTP	КАМАҮА	1.5A 32V	-	

Note1: There are different power supply systems from the power input terminal. The power supply capacity should be less than the fusing current. If the power supply capacity is above the fusing current, the fuse may blow in a short time, and then nasty smell, smoking and so on may occur.

The information contained herein is the exclusive property of SHANGHAI AVIC OPTOELECTRONICS Corporation, and shall not be distributed, reproduced, or disclosed in whole or in part without prior written permission of SHANGHAI AVIC OPTOELECTRONICS Corporation.

Note1 Note2



Q/S1021-2011

SHANGHAI AVIC OPTOELECTRONICS

10. OPTICS

10.1 Optical characteristics

Note I, I								
Parameter I	Note1	Condition	Symbol	min.	typ.	max.	Unit	Remarks
Luminan	ice	White at center θR=0°, θL=0° θU=0°, θD=0°	L	-	450		cd/m ²	-
Contrast ratio		White/Black at center θR=0°, θL=0° θU=0°, θD=0°	CR	_	700		_	Note3
Luminance ur	niformity	White θR=0°, θL=0° θU=0°, θD=0	LU	A.	1.25	(1.33)	3) - No	
	White	X coordinate	Wx		0.313		-	
	vvinte	Y coordinate	Wy	Þ	0.329		-	
	Red	X coordinate	Rx	-	TBD	-	-	
Chromaticity	Reu	Y coordinate	Ry	-	TBD	-	-	
Chromaticity	Green	X coordinate	Gx	-	TBD	-	-	
	Green	Y coordinate	Gy	-	TBD	-	-	Note5
	Blue	X coordinate	Bx	-	TBD	-	-	
	Diue	Y coordinate	Ву	-	TBD	-	-	
Color gamut		θR=0°, θL=0°, θU=0°, θD=0 At center,against NTSC	С	-	55	-	%	
		White to black	Ton	-	10	(20)	ms	Note6
Response	time	Black to white	Toff	-	25	(30)	ms	Note7
		Ton+ Toff	-	-	35	(50)	ms	NOLET
	Right	θU=0°, θD=0°, CR≥10	θR	-	80	-	o	
Viewing	Left	θU=0°, θD=0°, CR≥10	θL	-	80	-	o	Note8
angle	Up	θR=0°, θL=0°, CR≥10	θU	-	65	-	o	NOLEO
	Down	θR=0°, θL=0°, CR≥10	θD	-	75	-	o	

The information contained herein is the exclusive property of SHANGHAI AVIC OPTOELECTRONICS Corporation, and shall not be distributed, reproduced, or disclosed in whole or in part without prior written permission of SHANGHAI AVIC OPTOELECTRONICS Corporation.



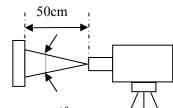
Q/S1021-2011

Note1: The values in upper table are only initial characteristics.

Note2: Measurement conditions are as follows.

- Ta= 25°C, VDD= 3.3V, IBL= 80mArms/led, Display mode: VGA,
- Horizontal cycle=55.56KHz, Vertical cycle=60.0Hz

Optical characteristics are measured at luminance saturation after 30minutes from working the product in the dark room. Also measurement method for luminance is as follows.



Luminance Meter (TOPCON BM-5A) Spectroradiometer(TOPCON SR-3)

LCD module (Product)

- Note 3: See"10.2 Definition of contrast ratio".
- Note 4: See "10.3 Definition of luminance uniformity".
- Note 5: CIE 1931 Chromaticity Diagram Standard.
- Note 6: Product surface temperature: TopF = 33.0 °C
- Note 7: See "10.4 Definition of response time".

Note 8: See "10.5 Definition of viewing angle".

10.2 Definition of contrast ratio

The contrast ratio is calculated by using the following formula. Contrast ratio (CR) = Luminance of white screen

Luminance of black screen

The information contained herein is the exclusive property of SHANGHAI AVIC OPTOELECTRONICS Corporation, and shall not be distributed, reproduced, or disclosed in whole or in part without prior written permission of SHANGHAI AVIC OPTOELECTRONICS Corporation.

Page 21 of 32

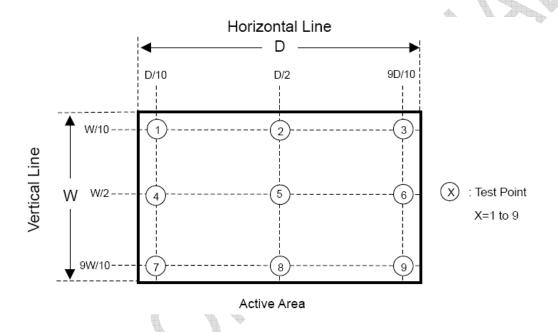


Q/S1021-2011

10.3 Definition of luminance uniformity

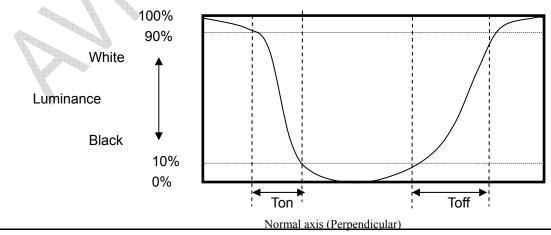
The luminance uniformity is calculated by using the following formula. Luminance uniformity (LU) =<u>Maximum luminance from (1) to (9)</u> Minimum luminance from (1) to (9)

The luminance is measured at near the 9 points shown below.



10.4 Definition of response times

Response time is measured, the luminance changes from "white" to "black", or "black" to "white" on the same screen point, by photo-detector. Ton is the time it takes the luminance change from 90% down to 10%. Also Toff is the time it takes the luminance change from 10% up to 90%. (See the following diagram.)



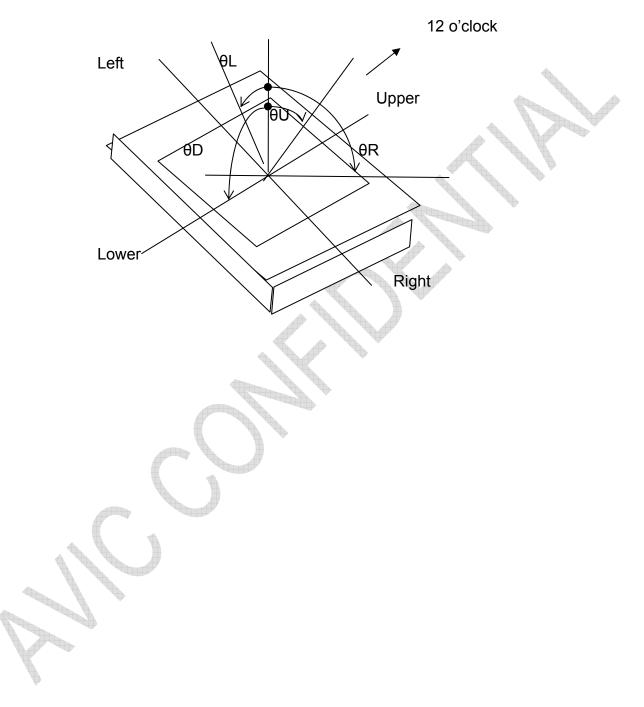
The information contained herein is the exclusive property of SHANGHAI AVIC OPTOELECTRONICS Corporation, and shall not be distributed, reproduced, or disclosed in whole or in part without prior written permission of SHANGHAI AVIC OPTOELECTRONICS Corporation.

Page 22 of 32



Q/S1021-2011

10.5 Definition of viewing angles



The information contained herein is the exclusive property of SHANGHAI AVIC OPTOELECTRONICS Corporation, and shall not be distributed, reproduced, or disclosed in whole or in part without prior written permission of SHANGHAI AVIC OPTOELECTRONICS Corporation.

Page 23 of 32



Q/S1021-2011

11. MARKINGS

The various markings are attached to this product. See "11.2 INDECATION LOCATIONS" for attachment positions.

11.1 PRODUCT LABEL

1.1 PI	RODU	CT LAE	BEL						/	UL M	ARK	
		02A ₩₩₩		9100 					E250 IN CH		R	OHS M
Note1: T	The mea	aning of	OEM nu	mber, E	xample:	S190M	50A12S	SA1SA1	09A0001			
S19	90M5	50A	12	2	S	SA18	SA1	<∕∕	09A		000	1
	ule Num		Source	e & Gate IC Code		ocation L			te code	S	erial Nun	nber
	e code: Charac	ter Year	Codes									
Month	2010	2011	2012	2013	2014	2015	2016	2017	2018	So on]	
Code	0	1	2	3	4	5	6	7	8		1	
2nc	Charao	cter Mon	th Code	s								
Month	January	February	March	April	May	June	July	August	September	October	November	Decemb
Code	1	2	3	4	5	6	7	8	9	А	В	С
3rd	Charac	ter Day	Codes					ł				
Day	1st	2nd	3rd	4th	5th	6th	7th	8th	9th	10th	11st	12nd
Code	1	2	3	4	5	6	7	8	9	A	В	C
F	10.1		4.5.1		1 = 1	101	40.1	a 0.1				
ŀ	13rd D	14th E	15th F	16th G	17th H	18th J	19th K	20th L	21st M	22nd N	23rd P	24th Q
L	2	2	•	3			IX.			1,		×
ļ	25th	26th	27th	28th	29th	30th	31st					
	R	S	Т	U	V	W	Х	1				

Note2: Do not attach anything such as label and so on, on the product label! In case repair the product, AVIC needs the contents of product label such as the lot number, inspection date and so on, to identify the warranty period with individual product. If AVIC cannot decipher the contents of product label, such repair shall be entitled to charge. Also AVIC may give a new lot number to reconditioned products.

The information contained herein is the exclusive property of SHANGHAI AVIC OPTOELECTRONICS Corporation, and shall not be distributed, reproduced, or disclosed in whole or in part without prior written permission of SHANGHAI AVIC OPTOELECTRONICS Corporation.



Q/S1021-2011

11.2 INDICATION LOCATIONS

Product rear side			
Disposal method marking	Barcode label	Product label	

The information contained herein is the exclusive property of SHANGHAI AVIC OPTOELECTRONICS Corporation, and shall not be distributed, reproduced, or disclosed in whole or in part without prior written permission of SHANGHAI AVIC OPTOELECTRONICS Corporation.

Page 25 of 32



Q/S1021-2011

12. PACKING, TRANSPORTATION AND DELIVERY

AVIC will pack products to deliver to customer in accordance with AVIC packing specifications, and will deliver products to customer in such a state that products will not suffer from a damage during transportation .The delivery conditions are as follows.

12.1 PACKING

- (1) Packing box
- 8 products are packed up with the maximum in a packing box(See "12.5 OUTLINE FIGURE FOR PACKING ").

Products are put into a plastic bag for prevention of moisture with cushion, and then the bag is sealed up with heat sealing.

The type name and quality are shown on outside of the packing box, either labeling or printing.

(2) Pallet Packing (See"12.5 OUTLINE FIGURE FOR PACKING ")

- ① Packing boxes are tired on a cardboard pallet.(8 boxes×4 tiers maximum)
- ② Cardboard sleeve and top cap are attached to the packing boxes, then they are fixed by a band.

12.2 INSPECTION RECORD SHEET

Inspection record sheets are included in the packing box with delivery products to customer. It is summarized to a number of products for pass/fail assessment.

12.3 TRANSPORTATION

The product is transported by vehicle, aircraft or shipment in the state of pallet packing.

12.4 SIZE AND WEIGHT FOR PACKING BOX

Parameter	Packing box	Unit
Size	TBD	mm
Weight	TBD	kg
Total weight	TBD	kg
		<u> </u>

The information contained herein is the exclusive property of SHANGHAI AVIC OPTOELECTRONICS Corporation, and shall not be distributed, reproduced, or disclosed in whole or in part without prior written permission of SHANGHAI AVIC OPTOELECTRONICS Corporation.



Q/S1021-2011

12.5 OUTLINE FIGURE FOR PACKING

TBD

The information contained herein is the exclusive property of SHANGHAI AVIC OPTOELECTRONICS Corporation, and shall not be distributed, reproduced, or disclosed in whole or in part without prior written permission of SHANGHAI AVIC OPTOELECTRONICS Corporation.

Page 27 of 32



Q/S1021-2011

13. PRECAUTIONS

13.1 MEANING OF CUTION SIGNS

The following caution signs have very important meaning .Be sure to read "13.2 CAUTIONS" and "13.3 ATTENTIONS", after understanding these contents!



This sign have the meaning that customer will be injured by himself or the product will sustain a damage, if customer has wrong operations.



This sign has the meaning that customer will get an electrical shock, if customer has wrong operations.



This sign has the meaning that customer will be injured by himself, if customer has wrong operations.

13.2 CAUTIONS



Do not touch lamp cables while turn on .Customers will be in danger of an electric shock

 * Do not touch the working backlight and IC. Customers will be in danger of burn injury.
 * Do not shock and press the LCD panel and the backlight! There is a danger of breaking, because they are made of glass.(shock :To be not greater 294m/s² and to be not greater 11ms, Pressure: To be not greater 19.6N)

13.3 ATTENTIONS

- 13.3.1 Handling of the product
- Take hold of both ends without touch the circuit board when customer pulls out products (LCD modules) from inner packing box. If customer touches it, products may be broken down or out of adjustment, because of stress to mounting parts.
- ② Do not hook cables nor pull connection cables such as flexible cable and so on , for fear of damage.
- ③ If customer puts down the product temporarily, the product puts on flat subsoil as a display side turns down.
- ④ Take the measures of electrostatic discharge such as earth band, ionic shower and so on, when customer deal with the product, because products may be damaged by electrostatic.

The information contained herein is the exclusive property of SHANGHAI AVIC OPTOELECTRONICS Corporation, and shall not be distributed, reproduced, or disclosed in whole or in part without prior written permission of SHANGHAI AVIC OPTOELECTRONICS Corporation.

Page 28 of 32



O/S1021-2011

- (5) The torque for mounting screws must never exceed 0.34N-m. Higher torque values might result in distortion of the bezel.
- ⑥ The product must be installed using mounting holes without undue stress such as bends or twist (See outline drawings).And do not add undue stress to any portion (such as bezel flat area) except mounting hole portion.
- ⑦ Bends or twist described above and undue stress to any portion except mounting hole portion may cause display un-uniformity.
- ③ Do not press or rub on the sensitive display surface .If customer clean on the panel surface, AVIC recommends using the cloth with ethanolic liquid such as screen cleaner for LCD.
- Do not push-pull the interface connectors while the product is working, because wrong power sequence may break down the product.
- Do not bend or unbend the lamp cable at the near part of the lamp holding rubber, to avoid the damage for high voltage side of the lamp. This damage may cause a lamp breaking and abnormal operation of high voltage circuit.

13.3.2 Environment

- ① Do not operate or store in high temperature, high humidity, dewdrop atmosphere or corrosive gases. Keep the product in antistatic pouch in room temperature, because of avoidance for dusts and sunlight, if customer stores the product.
- ② In order to prevent dew condensation occurring by temperature difference, the product packing box must be opened after leave under the environment of an unpacking room temperature enough. Because a situation of dew condensation occurring is changed by the environment temperature and humidity, evaluate the leaving time sufficiently. (Recommendation leaving time: 6 hour or more with packing state)
- ③ Do not operate in a high magnetic field .Circuit boards may be broken down by it.
- ④ This product is not designed as radiation hardened.
- ⁽⁵⁾ Use an original protection sheet on the product surface (polarizer). Adhesive type protection sheet should be avoided, because it may change color or properties of the polarizer.

13.3.3 Characteristics

The following items are neither defects nor failures.

- ① ambient temperature.
- 2 The LCD may be seemed luminance non-uniformity, flicker, vertical seam or small spot by display patterns.
- 3 Optical characteristics (e.g. luminance, display uniformity, etc.) gradually is going to change depending on operating time ,and especially low temperature, because the LCD has cold cathode fluorescent lamps.
- ④ Do not display the fixed pattern for a long time because it may cause image sticking .Use a screen saver, if the fixed pattern is displayed on the screen.
- ⁽⁵⁾ The display color may be changed by viewing angle because of the use of condenser sheet in the backlight.
- 6 Optical characteristics may be changed by input signal timings.

The information contained herein is the exclusive property of SHANGHAI AVIC OPTOELECTRONICS Corporation, and shall not be distributed, reproduced, or disclosed in whole or in part without prior written permission of SHANGHAI AVIC OPTOELECTRONICS Corporation.

Page 29 of 32



Q/S1021-2011

⑦ The interference noise of input signal frequency for this product and luminance control frequency of customer's backlight inverter may appear on a display. Set up luminance control frequency of backlight inverter so that the interference noise doses not appear.

13.4 Other

- ① All GND and VCC terminals should be used without a non-connected line.
- 2 Do not disassemble a product or adjust volume without permission of AVIC.
- ③ Pay attention not to insert waste materials inside of products, if customer uses screw nails.
- ④ Pack the product with original shipping package, because of avoidance of some damages during transportation, when customer returns it to AVIC for repair and so on .
- Solution Solution

The information contained herein is the exclusive property of SHANGHAI AVIC OPTOELECTRONICS Corporation, and shall not be distributed, reproduced, or disclosed in whole or in part without prior written permission of SHANGHAI AVIC OPTOELECTRONICS Corporation.

Page 30 of 32

۲

12



SHANGHAI AVIC OPTOELECTRONICS Q/S1021-2011 **14.OUTDRAWING** ð 2.Z 5.Z3 6.2Z 9(typ) l N 88.8±0.1 97±0,1 88+N নি 16.5 ActiveArea246*184.5 ມີ <u>139,5±0,1</u> 1<u>37,5±0,1</u> 137.5 ± 0.0 36.5±0. 36.5±0. 249.05±0.3(BEZEL OPENING 279±0.5(MODULE OUTLINE) 246(ACTIVE AREA) 76±0.5 ACTIVE 13.85 15.75 <u>133.8±0.1</u> 135.5±0.1 AREA AREA CENTER 33.8 ± 0.1 35.5±0. CONTROL DIMENSION REFERENCE DIMENSION DESIGNED HECKED PROVED LONG YUAN DONGHUA CHEN DEKAI CHEN 2011.05.27 2011.05.27 74±0. 64±0,1 2011.05.27 RE< 83±0.1 83±0.1 DC/EC NUMBER 184.5(ACTIVE AREA) MATERIAL NUMBER PART NAME DRAWING NUMBER PREDUCT NUMBER 188.3±0.3(BEZEL DPENING AREA) 209±0.5(MDDULE **DUTLINE** 9(typ) 121LED DUTLINE 27 TIANMA DESCRIPTION -3rd ANGLE SCALE INU PAGE Ψ DAT Ē

The information contained herein is the exclusive property of SHANGHAI AVIC OPTOELECTRONICS Corporation, and shall not be distributed, reproduced, or disclosed in whole or in part without prior written permission of SHANGHAI AVIC OPTOELECTRONICS Corporation.

ω

9

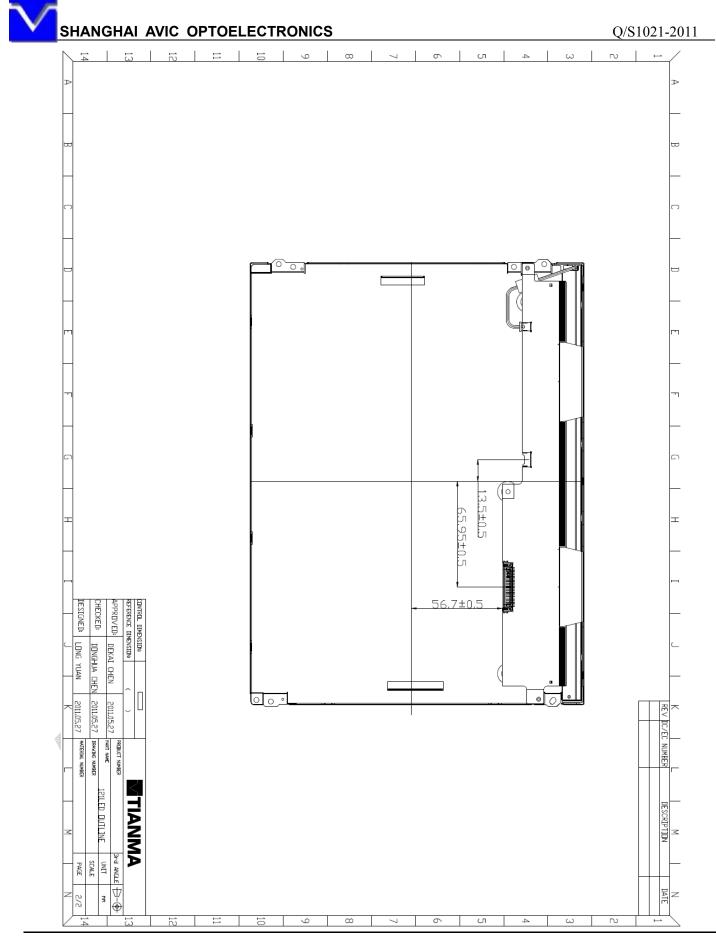
σ

J

ω

Page 31 of 32





The information contained herein is the exclusive property of SHANGHAI AVIC OPTOELECTRONICS Corporation, and shall not be distributed, reproduced, or disclosed in whole or in part without prior written permission of SHANGHAI AVIC OPTOELECTRONICS Corporation.